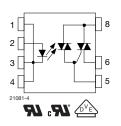


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Optocoupler, Power Phototriac





PIN	FUNCTION		
1	LED cathode		
2	LED anode		
3	LED cathode		
4	LED cathode		
5	Triac gate		
6	Triac T1		
8	Triac T2		

DESCRIPTION

The VO2223 is an optically couple phototriac driving a power triac in a DIP-8 package. It provides a 5300 V of input to output isolation.

FEATURES

- Maximum trigger current (I_{FT}): 10 mA
- Isolation test voltage 5300 V_{RMS}
- Peak off-state voltage 600 V
- Load current 0.9 A_{RMS}
- dV/dt of 210 V/μs
- DIP-8 package
- Pure tin leads
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Home appliances (air conditioners, microwave ovens, washing machines, personal hygiene systems, refrigerators, fan heaters, inductive heating cooker, water heaters, etc.)
- Industrial equipments

AGENCY APPROVALS

The safety application model number covering all products in this data sheet is VO2223. This model number should be used when consulting safety agency documents.

- UL E52744 system code H
- cUL E52744 system code H
- VDE DIN EN 60747-5-5 (VDE 0884-5)

ORDERING INFORMATION					
V 0 2 2 2	3 - X 0 0 #				
PART NUMBER	PACKAGE OPTION				
AGENCY CERTIFIED/PACKAGE	TRIGGER, CURRENT I _{FT} (mA)				
UL, cUL	10				
DIP-8	VO2223				
VDE, UL, cUL	10				
DIP-8	VO2223-X001				



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
INPUT								
LED continuous forward current		I _F	50	mA				
LED reverse voltage		V_{R}	6	V				
OUTPUT								
Repetitive peak off-state voltage	Sine wave, 50 Hz to 60 Hz, gate open	V_{DRM}	600	٧				
On-state RMS current		I _{T(RMS)}	0.9	Α				
Peak non-repetitive surge current (60 Hz, 1 cycle)		I _{TSM}	9	Α				
COUPLER	COUPLER							
Total power dissipation (2)		P _{diss}	1.2	W				
Ambient temperature range		T _{amb}	-40 to +85	°C				
Storage temperature range		T _{stg}	-40 to +125	Ô				
Soldering temperature (1)	t ≤ 10 s max.	T _{sld}	260	°C				
Isolation test voltage	For 1 s	V _{ISO}	5300	V_{RMS}				

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability
- (1) Refer to wave profile for soldering conditions for through hole devices
- (2) Total power dissipation value is based on 2S2P PCB

ABSOLUTE MAXIMUM RATING CURVES

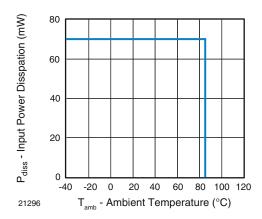


Fig. 1 - Power Dissipation vs. Temperature

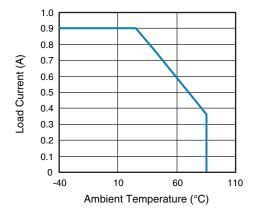


Fig. 2 - Allowable Load Current vs. Ambient Temperature

Note

 The allowable load current was calculated out under a given operating conditions and only for reference:
 LED power: Q_E = 0.015 W, θ_{BA} (4-layer) = 35 °C/W

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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
LED trigger current	V _T = 6 V	I _{FT}	2.5	-	10	mA	
LED reverse current	V _R = 5 V	I _R	-	-	10	μΑ	
LED forward voltage	I _F = 10 mA	V_{F}	0.9	-	1.3	V	
OUTPUT							
Peak on-state voltage	$I_F = 10 \text{ mA}, I_{TM} = \text{max}.$	V_{TM}	-	-	2.5	V	
Peak off-state current	$I_F = 10 \text{ mA}, V_{DRM} = 600 \text{ V}$	I _{DRM}	1	-	100	μΑ	
Holding current	$R_L = 100 \Omega$	I _H	-	-	25	mA	
Critical rate of rise of off-state voltage	$V_{IN} = 400 V_{RMS}$ (Fig. 3)	dV/dt _{cr}	ı	210	1	V/µs	
Critical rate of rise of commutating voltage	$V_{IN} = 240 V_{RMS}, I_T = 1 A_{RMS}$ (Fig. 3)	dV/dt _{crq}	- 1	0.7	- 1	V/µs	

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements

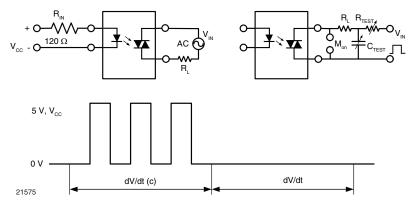


Fig. 3 - dV/dt Test Circuit

SAFETY AND INSULATION RATINGS								
PARAMETER		TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Climatic classification		IEC 68 part 1		-	40 / 85 / 21	-		
Pollution degree		DIN VDE0109		-	2	-		
Tracking resistance (compa	Tracking resistance (comparative tracking index)		CTI	175	-	-		
Highest allowable overvoltage		Transient overvoltage	V _{IOTM}	8000	-	-	V _{peak}	
Maximum working insulation voltage		Recurring peak voltage	V_{IORM}	890	-	-	V _{peak}	
Insulation resistance at 25 °C		V _{IO} = 500 V	R _{IS}	-	-	≥ 10 ¹²	Ω	
Insulation resistance at T _S		V _{IO} = 500 V	R _{IS}	-	-	≥ 10 ⁹	Ω	
Insulation resistance at 100 °C		V _{IO} = 500 V	R _{IS}	-	-	≥ 10 ¹¹	Ω	
Partial discharge test voltage		Method b, $V_{pd} = V_{IORM} \times 1.6$	V_{pd}	-	-	1424	V _{peak}	
Safety limiting values - maximum values allowed in the event of a failure	Case temperature		T _{SI}	-	-	165	°C	
	Input current		I _{SI}	-	-	150	mA	
	Output power		P _{SO}	-	-	2000	mW	
Minimum external air gap (clearance distance)		Measured from input terminals to output terminals, shortest distance through air		≥7	-	ı	mm	
Minimum external tracking (creepage distance)		Measured from input terminals to output terminals, shortest distance path along body		≥7	-	-	mm	

Note

This phototriac coupler is suitable for "Safe Electrical Insulation" only within the safety ratings. Compliance with safety ratings shall be
ensured by means of protective circuits



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TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

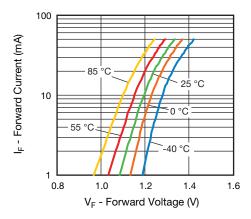


Fig. 4 - Forward Current vs. Forward Voltage

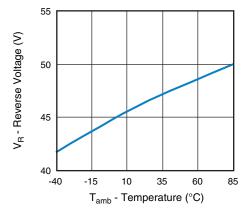


Fig. 5 - Reverse Voltage vs. Temperature

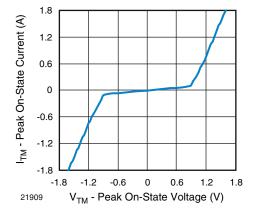


Fig. 6 - On-State Current vs. On-State Voltage

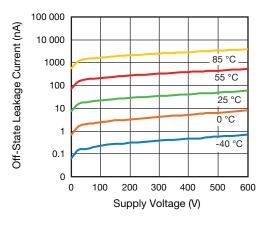


Fig. 7 - Off-State Leakage Current vs. Voltage

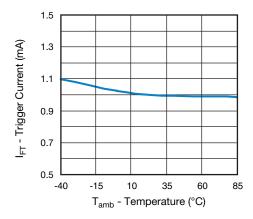


Fig. 8 - Normalized Trigger Input Current vs. Temperature

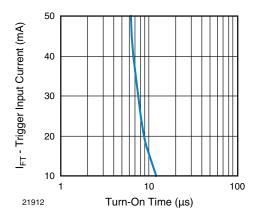


Fig. 9 - Trigger Input Current vs. Turn-On Time

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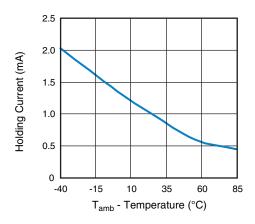


Fig. 10 - Normalized Holding Current vs. Temperature

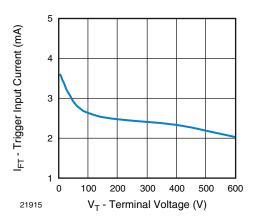


Fig. 12 - Trigger Current vs. V_{LOAD}

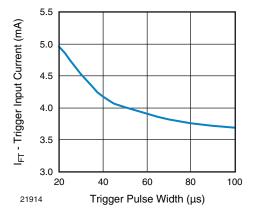
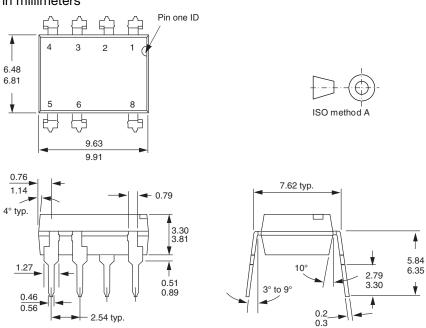


Fig. 11 - Trigger Current vs. Trigger Pulse Width

PACKAGE DIMENSIONS in millimeters

i178006-1

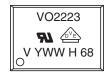


Rev. 1.4, 07-Dec-17 5 Document Number: 81166



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PACKAGE MARKING (Example of VO2223-X001)





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