

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHC05F, TC74VHC05FK

Hex Inverter (open drain)

The TC74VHC05 is an advanced high speed CMOS INVERTER fabricated with silicon gate $\rm C^2MOS$ technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

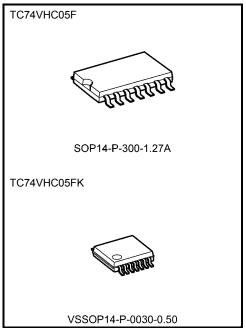
Pin configuration and function are the same as the TC74VHC04, but the TC74VHC05 has high performance MOS N-channel transistor. (OPEN-DRAIN outputs)

This device can, therefore, with a suitable pull-up resistors, be used in wired-AND, LED drive and other applications.

An input protection circuit ensures that 0 to $5.5 \, V$ can be applied to the input pins without regard to the supply voltage. This device can be used to interface $5 \, V$ to $3 \, V$ systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

Features

- High speed: $t_{pZ} = 3.8 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 2 \mu A \text{ (max)}$ at $T_a = 25 \text{°C}$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Power down protection is provided on all inputs.
- Wide operating voltage range: V_{CC} (opr) = 2 to 5.5 V
- Low noise: VOLP = 0.8 V (max)
- Pin and function compatible with 74ALS05



Weight

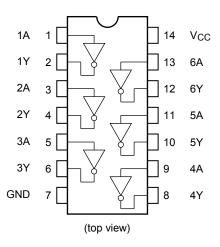
SOP14-P-300-1.27A : 0.18 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

Start of commercial production 1998-06

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Pin Assignment



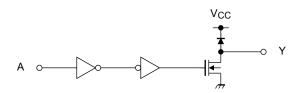
IEC Logic Symbol

1A (1)	1 ^	(2) 1Y
2A (3)	1 \(\sigma\)	(4) 2Y
3A (5)		(6) 3Y
3A		(2)
4A - /		~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~
DA — /		
6A (13)		(12) 6Y

Truth Table

Α	Y
L	Z
Н	L

System Diagram (per gate)



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	−0.5 to 7.0	V
DC input voltage	VIN	−0.5 to 7.0	V
DC output voltage	Vout	-0.5 to V _{CC} + 0.5	٧
Input diode current	lıĸ	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	−65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2.0 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	Vout	0 to V _{CC}	V
Operating temperature	Topr	−40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 (V _{CC} = 3.3 ± 0.3 V) 0 to 20 (V _{CC} = 5 ± 0.5 V)	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device.
Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Condition		Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit	
onaractoriotics	Cymbol			Vcc (V)	Min	Тур.	Max	Min	Max	Onit
High-level input voltage	ViH	_		2.0 3.0 to 5.5	1.50 V _{CC} × 0.7		_ _	1.50 V _{CC} × 0.7		٧
Low-level input voltage	VIL	_		2.0 3.0 to 5.5	_		0.50 V _{CC} × 0.3		0.50 V _{CC} × 0.3	>
Low-level output voltage Vol Vin	I _{OL} = 50 μA		2.0 3.0 4.5	_ _ _	0.0 0.0 0.0	0.1 0.1 0.1		0.1 0.1 0.1	V	
		I _{OL} = 4 mA I _{OL} = 8 mA	3.0 4.5	_	_	0.36 0.36	_	0.44 0.44		
Output off-state current	loz	V _{IN} = V _{IL} V _{OUT} = V _{CC} or GND		5.5	_	ı	±0.25	ı	±2.50	μΑ
Input leakage current	liN	V _{IN} = 5.5 V or GND		0 to 5.5	_		±0.1		±1.0	μΑ
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μΑ



AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics Symbol		Test Condition			Ta = 25°C			Ta = −40 to 85°C		Unit
			V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Onne
			3.3 ± 0.3	15	_	5.0	7.1	1.0	8.5	
Propagation delay	+	D. = 1 kO		50	_	7.5	10.6	1.0	12.0	no
time tpZL	ιρZL	R _L = 1 kΩ	5.0 ± 0.5	15	_	3.8	5.5	1.0	6.5	ns
				50	_	5.3	7.5	1.0	8.5	
Propagation delay	+=	R _L = 1 kΩ	3.3 ± 0.3	50		7.5	10.6	1.0	12.0	20
time	ιρLZ		5.0 ± 0.5	50	1	5.3	7.5	1.0	8.5	ns
Input capacitance	CIN		_		_	4	10	_	10	pF
Output capacitance	Соит		_		1	5	_	_	1	pF
Power dissipation capacitance	CPD			(Note)		6	_	_		pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

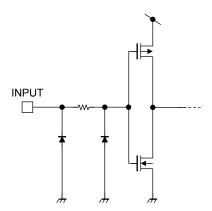
Average operating current can be obtained by the equation:

ICC (opr) = CPD·VCC·fIN + ICC/6 (per gate)

Noise Characteristics (input: $t_r = t_f = 3$ ns)

Characteristics	Currah al	Test Condition		Ta =	Linit	
Characteristics	Symbol		V _{CC} (V)	Тур.	Limit	Unit
Quiet output maximum dynamic VoL	VOLP	C _L = 50 pF	5.0	0.4	0.8	V
Quiet output minimum dynamic V _{OL}	Volv	C _L = 50 pF	5.0	-0.4	-0.8	V
Minimum high level dynamic input voltage	VIHD	C _L = 50 pF	5.0	_	3.5	V
Maximum low level dynamic input voltage	VILD	C _L = 50 pF	5.0	_	1.5	V

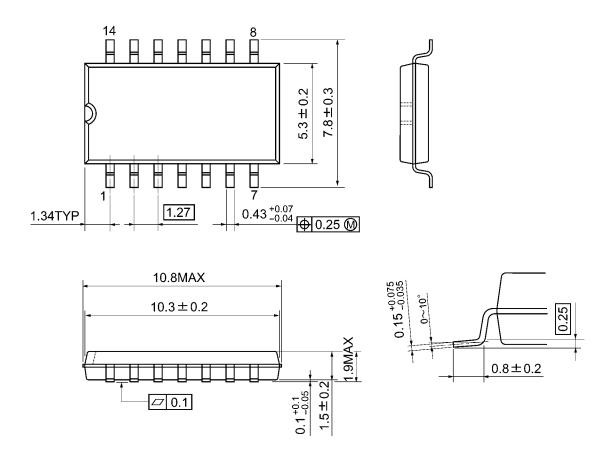
Input Equivalent Circuit





Package Dimensions

SOP14-P-300-1.27A Unit: mm

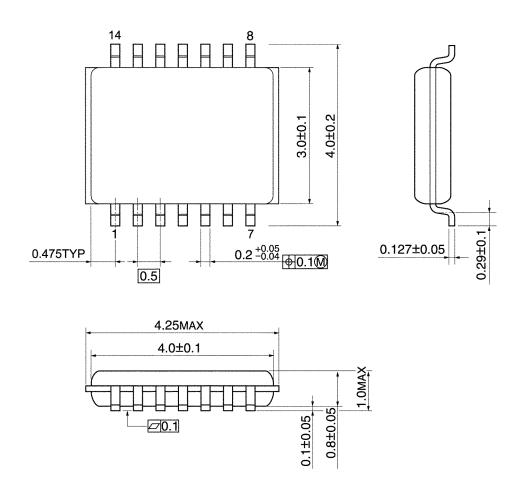


Weight: 0.18 g (typ.)



Package Dimensions

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)



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