

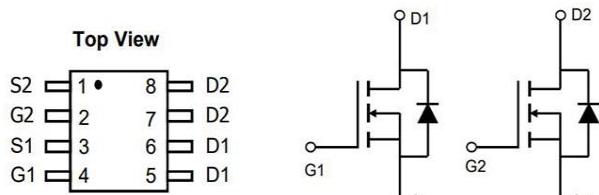
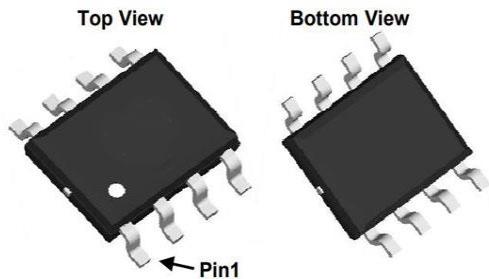
General Description

60V /15A Dual 2N Power MOSFET

Very low on-resistance RDS(on) @ VGS=4.5 V

Pb-free lead plating; RoHS compliant

V_{DS}	60	V
R_{DS(on)},TYP@VGS=10V	14.0	mΩ
R_{DS(on)},TYP@VGS=4.5	25.0	mΩ
I_D	15	A



Part ID	Package Type	Marking	Tape and reel infomation
AC4828	SOP8	4828	3000

100% UIS Tested
100% RG Tested

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	20	±V
Continuous Drain Current A	I _D	15.0	A
		12.0	
Pulsed Drain Current B	I _{DM}	24.0	A
Avalanche Current G	I _{AR}	4.8	
Repetitive avalanche energy L=0.1mH G	E _{AR}	11.0	mJ
Power Dissipation A	P _D	30	W
		25	
Junction and Storage Temperature Range	T _J , T _{STG}	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient A	R _{θJA}	192	288	°C/W
Maximum Junction-to-Ambient A		385	462	°C/W
Maximum Junction-to-Lead c	R _{θJL}	115	184	°C/W

STATIC PARAMETERS

Symbol	Parameter	Conditions	Min	Typ	Max	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D = -250\mu A, V_{GS} = 0V$	60			V
Id_{SS}	Zero Gate Voltage Drain Current	$V_{DS}=60V, V_{GS}=0V$			1	uA
					5	
I_{GSS}	Gate-Body leakage current	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	1	1.5	2.5	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, ID=10A$		14.0	18.0	$m\Omega$
		$V_{GS}=4.5V, ID=10A$		25.0	32.5	
g_{FS}	Forward Transconductance	$V_{DS}=5V, ID=4.5A$		78		S
V_{SD}	Diode Forward Voltage	$IS=1A, V_{GS}=13V$		0.72	1	V
I_S	Maximum Body-Diode Continuous Current				4.5	A

DYNAMIC PARAMETERS

Symbol	Parameter	Conditions	Min	Typ	Max	Units
C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=15V, f=1MHz$		450	549	pF
C_{oss}	Output Capacitance			60	73	pF
C_{rss}	Reverse Transfer Capacitance			25	29	pF
R_g	Gate resistance	$V_{GS}=0V, V_{DS}=0V, f=1MHz$			1.1	Ω

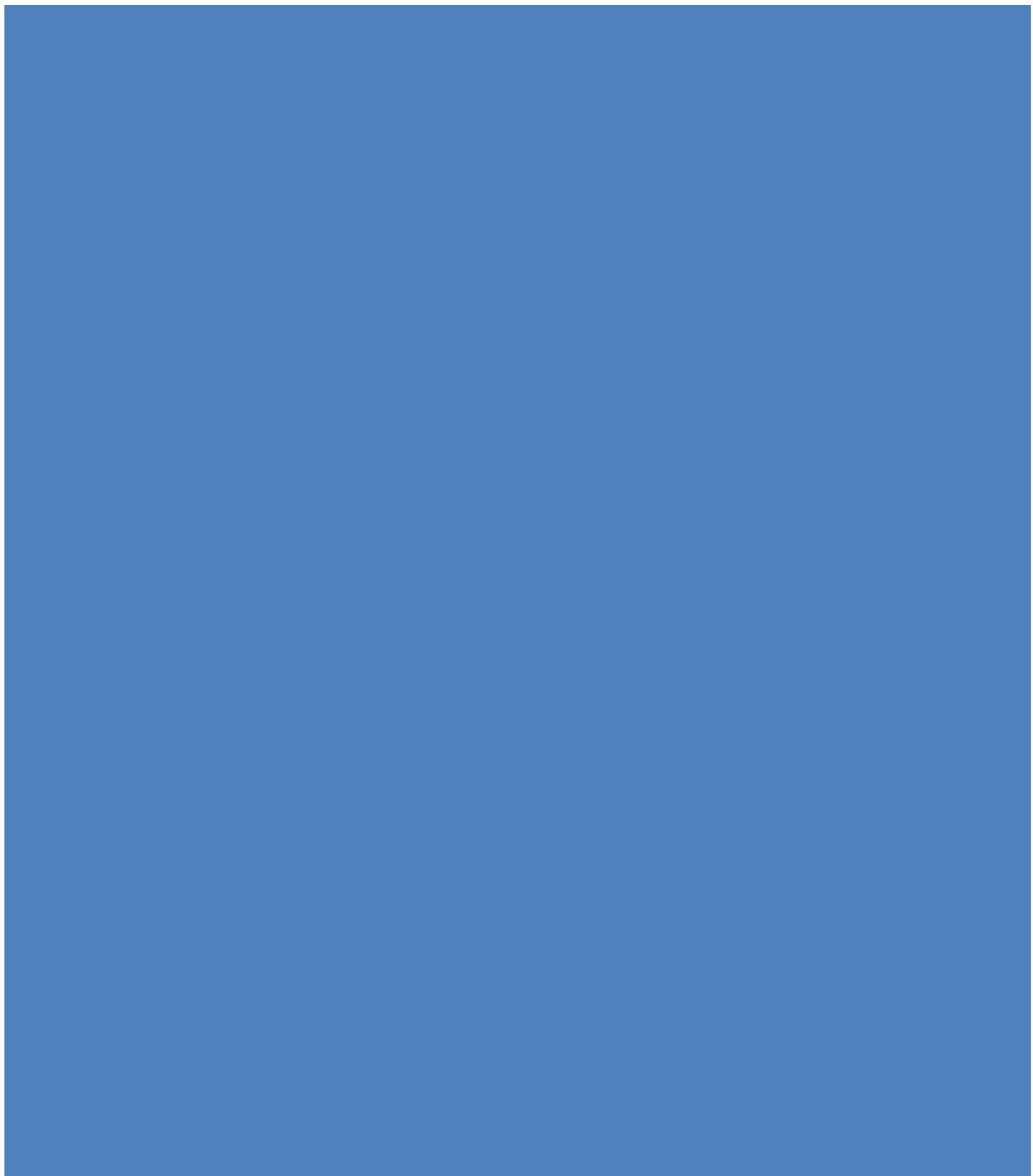
SWITCHING PARAMETERS

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$Q_g(10V)$	Total Gate Charge	$V_{GS}=10V, V_{DS}=15V, ID=4.5A$		4.3		nC
$Q_g 4.5V)$	Total Gate Charge			2.15		
Q_{gs}	Gate Source Charge			1.54		
Q_{gd}	Gate Drain Charge			2.2		
$t_{D(on)}$	Turn-On Delay Time	$V_{GS}=10V, V_{DS}=15V, RL=0.75\Omega, R_{GEN}=3\Omega$		13.75		ns
t_r	Turn-On Rise Time			11		
$t_{D(off)}$	Turn-Off Delay Time			38.5		
t_f	Turn-Off Fall Time			12.375		
t_{rr}	Body Diode Reverse Recovery Time	$I_F=-8A, dI/dt=500A/\mu s$		27.5		ns
Q_{rr}	Body Diode Reverse Recovery Charge	$I_F=18A, dI/dt=500A/\mu s$		32		nC



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