Dual 2-input AND gate Rev. 6 — 1 November 2018

### 1. General description

The 74HC2G08; 74HCT2G08 is a dual 2-input AND gate. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of  $V_{CC}$ .

### 2. Features and benefits

- Wide supply voltage range from 2.0 V to 6.0 V
- Input levels:
  - For 74HC2G08: CMOS level
  - For 74HCT2G08: TTL level
- Symmetrical output impedance
- High noise immunity
- Low power dissipation
- Balanced propagation delays
- Multiple package options
- ESD protection:
  - HBM JESD22-A114E exceeds 2 000 V
  - MM JESD22-A115-A exceeds 200 V
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

### 3. Ordering information

Table	1.	Ordering	information	

Type number Package										
	Description	Version								
74HC2G08DP	-40 °C to +125 °C	TSSOP8	plastic thin shrink small outline package; 8 leads;	SOT505-2						
74HCT2G08DP			body width 3 mm; lead length 0.5 mm							
74HC2G08DC	-40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads;	SOT765-1						
74HCT2G08DC	_		body width 2.3 mm							

### 4. Marking

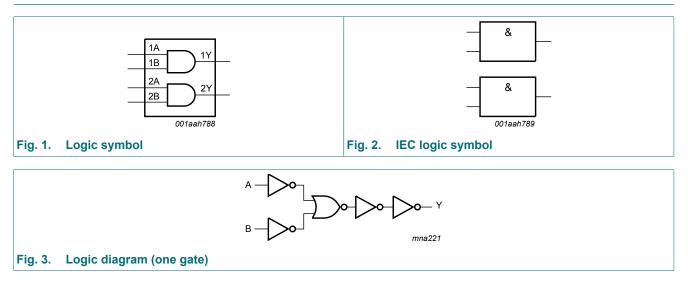
#### Table 2. Marking code

Type number	Marking code[1]
74HC2G08DP	H08
74HCT2G08DP	Т08
74HC2G08DC	H08
74HCT2G08DC	Т08

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

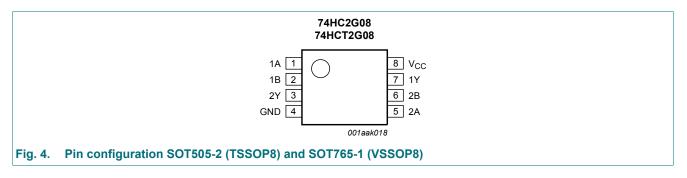
# nexperia

### 5. Functional diagram



### 6. Pinning information

#### 6.1. Pinning



### 6.2. Pin description

#### Table 3. Pin description

Symbol	Pin	Description
1A, 2A	1, 5	data input
1B, 2B	2, 6	data input
GND	4	ground (0 V)
1Y, 2Y	7, 3	data output
V <sub>CC</sub>	8	supply voltage

### 7. Functional description

#### Table 4. Function table

H = HIGH voltage level; L = LOW voltage level.

Input	Output	
nA	nB	nY
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Мах	Unit
V <sub>CC</sub>	supply voltage			-0.5	+7.0	V
I <sub>IK</sub>	input clamping current	$V_{\rm I}$ < -0.5 V or $V_{\rm I}$ > $V_{\rm CC}$ + 0.5 V	[1]	-	±20	mA
Ι <sub>ΟΚ</sub>	output clamping current	$V_{\rm O}$ < -0.5 V or $V_{\rm O}$ > $V_{\rm CC}$ + 0.5 V	[1]	-	±20	mA
lo	output current	$V_{\rm O}$ = -0.5 V to (V <sub>CC</sub> + 0.5 V)	[1]	-	25	mA
I <sub>CC</sub>	supply current		[1]	-	50	mA
I <sub>GND</sub>	ground current		[1]	-50	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>D</sub>	dynamic power dissipation	$T_{amb}$ = -40 °C to +125 °C	[2]	-	300	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For TSSOP8 package: above 55 °C the value of  $P_{tot}$  derates linearly with 2.5 mW/K.

For VSSOP8 package: above 110 °C the value of P<sub>tot</sub> derates linearly with 8 mW/K.

### 9. Recommended operating conditions

#### Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	74HC2G08			74HCT2G08			Unit
			Min	Тур	Max	Min	Тур	Max	
V <sub>CC</sub>	supply voltage		2.0	5.0	6.0	4.5	5.0	5.5	V
VI	input voltage		0	-	V <sub>CC</sub>	0	-	V <sub>CC</sub>	V
Vo	output voltage		0	-	V <sub>CC</sub>	0	-	V <sub>CC</sub>	V
T <sub>amb</sub>	ambient temperature		-40	+25	+125	-40	+25	+125	°C
Δt/ΔV	input transition rise and	V <sub>CC</sub> = 2.0 V	-	-	625	-	-	-	ns/V
	fall rate	V <sub>CC</sub> = 4.5 V	-	1.67	139	-	1.67	139	ns/V
		V <sub>CC</sub> = 6.0 V	-	-	83	-	-	-	ns/V

### **10. Static characteristics**

#### Table 7. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	-40	°C to +8	5 °C	-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	1
74HC2G	608	1					I	
V <sub>IH</sub> HIGH-level input		V <sub>CC</sub> = 2.0 V	1.5	1.2	-	1.5	-	V
	voltage	V <sub>CC</sub> = 4.5 V	3.15	2.4	-	3.15	-	V
V <sub>IL</sub>		V <sub>CC</sub> = 6.0 V	4.2	3.2	-	4.2	-	V
V <sub>IL</sub>	LOW-level input	V <sub>CC</sub> = 2.0 V	-	0.8	0.5	-	0.5	V
V <sub>IL</sub>	voltage	V <sub>CC</sub> = 4.5 V	-	2.1	1.35	-	1.35	V
		V <sub>CC</sub> = 6.0 V	-	2.8	1.8	-	1.8	V
V <sub>OH</sub>	HIGH-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$						
	voltage	I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 2.0 V	1.9	2.0	-	1.9	-	V
		I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 4.5 V	4.4	4.5	-	4.4	-	V
		I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 6.0 V	5.9	6.0	-	5.9	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 4.5 V	4.13	4.32	-	3.7	-	V
		I <sub>O</sub> = -5.2 mA; V <sub>CC</sub> = 6.0 V	5.63	5.81	-	5.2	-	V
V <sub>OL</sub>	LOW-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$						
	voltage	I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 2.0 V	-	0	0.1	-	0.1	V
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 4.5 V	-	0	0.1	-	0.1	V
		I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 6.0 V	-	0	0.1	-	0.1	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 4.5 V	-	0.15	0.33	-	0.4	V
		I <sub>O</sub> = 5.2 mA; V <sub>CC</sub> = 6.0 V	-	0.16	0.33	-	0.4	V
l <sub>l</sub>	input leakage current	$V_{I} = V_{CC}$ or GND; $V_{CC} = 6.0 V$	-	-	±1.0	-	±1.0	μA
I <sub>CC</sub>	supply current	per input pin; $V_I = V_{CC}$ or GND; $I_O = 0 A$ ; $V_{CC} = 6.0 V$	-	-	10	-	20	μA
CI	input capacitance		-	1.5	-	-	-	pF
74HCT2	G08	1					<u> </u>	
V <sub>IH</sub>	HIGH-level input voltage	$V_{CC}$ = 4.5 V to 5.5 V	2.0	1.6	-	2.0	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 4.5 V to 5.5 V	-	1.2	0.8	-	0.8	V
V <sub>OH</sub>	HIGH-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$						
	voltage	I <sub>O</sub> = -20 μA; V <sub>CC</sub> = 4.5 V	4.4	4.5	-	4.4	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 4.5 V	4.13	4.32	-	3.7	-	V
V <sub>OL</sub>	LOW-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$						
	voltage	I <sub>O</sub> = 20 μA; V <sub>CC</sub> = 4.5 V	-	0	0.1	-	0.1	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 4.5 V	-	0.15	0.33	-	0.4	V
l	input leakage current	$V_{I} = V_{CC}$ or GND; $V_{CC} = 5.5 V$	-	-	±1.0	-	±1.0	μA
I <sub>CC</sub>	supply current	$V_{I} = V_{CC}$ or GND; $I_{O} = 0$ A; $V_{CC} = 5.5$ V	-	-	10	-	20	μA
ΔI <sub>CC</sub>	additional supply current	per input; $V_{CC}$ = 4.5 V to 5.5 V; V <sub>I</sub> = V <sub>CC</sub> - 2.1 V; I <sub>O</sub> = 0 A	-	-	375	-	410	μA

#### **Dual 2-input AND gate**

Symbol	Parameter	Conditions	-40 °C to +85 °C		-40 °C to +125 °C		Unit	
			Min	Typ <mark>[1]</mark>	Мах	Min	Мах	
Cı	input capacitance		-	1.5	-	-	-	pF

[1] All typical values are measured at  $T_{amb}$  = 25 °C.

### **11. Dynamic characteristics**

#### Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 6.

Symbol	Parameter	Conditions		-40	°C to +85	5 °C	-40 °C t	Unit	
		-		Min	Typ[1]	Мах	Min	Max	1
74HC2G	08								
t <sub>pd</sub>	propagation delay	nA and nB to nY; see Fig. 5	[2]						
		V <sub>CC</sub> = 2.0 V		-	26	95	-	110	ns
		V <sub>CC</sub> = 4.5 V		-	9	19	-	22	ns
		V <sub>CC</sub> = 5.0 V; C <sub>L</sub> = 15 pF		-	9	-	-	-	ns
		V <sub>CC</sub> = 6.0 V		-	8	16	-	20	ns
t <sub>t</sub>	transition time	see Fig. 5	[3]						
		V <sub>CC</sub> = 2.0 V		-	20	95	-	125	ns
		V <sub>CC</sub> = 4.5 V		-	7	19	-	25	ns
		V <sub>CC</sub> = 6.0 V		-	6	16	-	20	ns
C <sub>PD</sub>	power dissipation capacitance	$V_I = GND$ to $V_{CC}$	[4]	-	10	-	-	-	pF
74HCT2	G08								
t <sub>pd</sub>	propagation delay	nA and nB to nY; see Fig. 5	[2]						
		V <sub>CC</sub> = 4.5 V		-	14	30	-	36	ns
		V <sub>CC</sub> = 5.0 V; C <sub>L</sub> = 15 pF		-	14	-	-	-	ns
t <sub>t</sub>	transition time	V <sub>CC</sub> = 4.5 V; see <u>Fig. 5</u>	[3]	-	7	19	-	22	ns
C <sub>PD</sub>	power dissipation capacitance	$V_{I}$ = GND to $V_{CC}$ - 1.5 V	[4]	-	10	-	-	-	pF

[1] All typical values are measured at  $T_{amb}$  = 25 °C.

[2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .

[3]  $t_t$  is the same as  $t_{TLH}$  and  $t_{THL}$ .

 $\label{eq:power} \begin{tabular}{c} [4] & C_{PD} \mbox{ is used to determine the dynamic power dissipation ($P_D$ in $\mu$W). \end{tabular}$ 

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$  where:

 $f_i$  = input frequency in MHz;

 $f_o$  = output frequency in MHz;

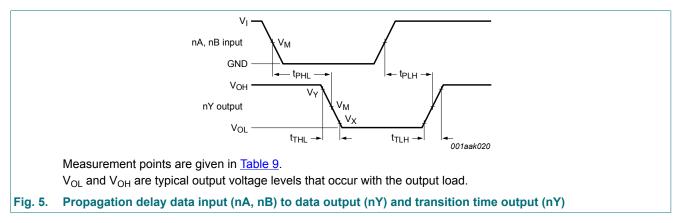
 $C_L$  = output load capacitance in pF;

 $V_{CC}$  = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_0)$  = sum of outputs.

### 11.1. Waveforms and test circuit

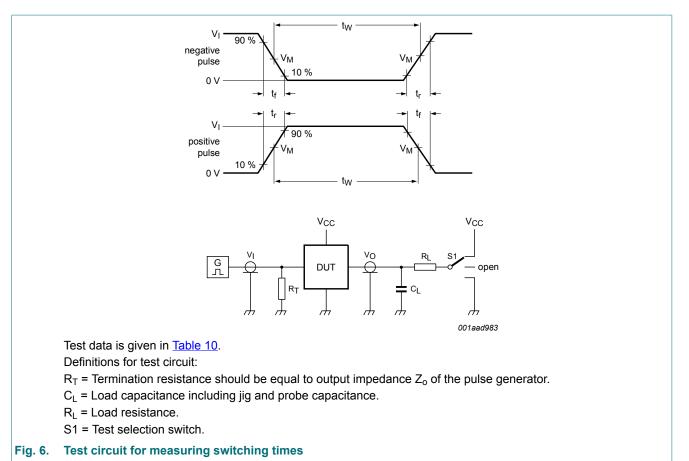


#### Table 9. Measurement points

Туре	Input	Output				
	V <sub>M</sub>	V <sub>M</sub>	V <sub>X</sub>	V <sub>Y</sub>		
74HC2G08	0.5V <sub>CC</sub>	0.5V <sub>CC</sub>	0.1V <sub>CC</sub>	0.9V <sub>CC</sub>		
74HCT2G08	1.3 V	1.3 V	0.1V <sub>CC</sub>	0.9V <sub>CC</sub>		

74HC\_HCT2G08

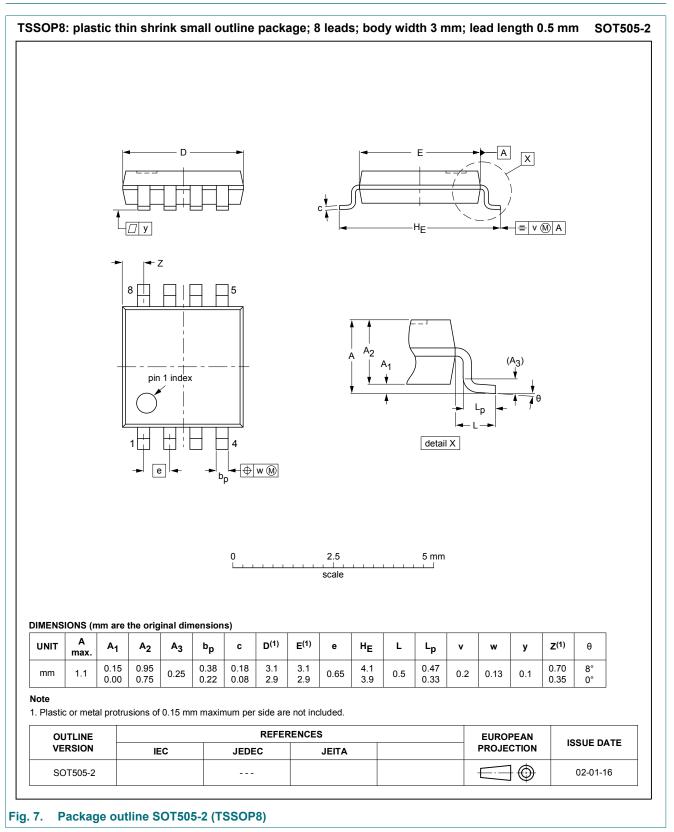
#### **Dual 2-input AND gate**



#### Table 10. Test data

Туре	Input Load			Load		
	VI	t <sub>r</sub> , t <sub>f</sub>	CL	RL	t <sub>PHL</sub> , t <sub>PLH</sub>	
74HC2G08	GND to V <sub>CC</sub>	≤ 6 ns	15 pF, 50 pF	1 kΩ	open	
74HCT2G08	GND to 3 V	≤ 6 ns	15 pF, 50 pF	1 kΩ	open	

### 12. Package outline



#### **Dual 2-input AND gate**

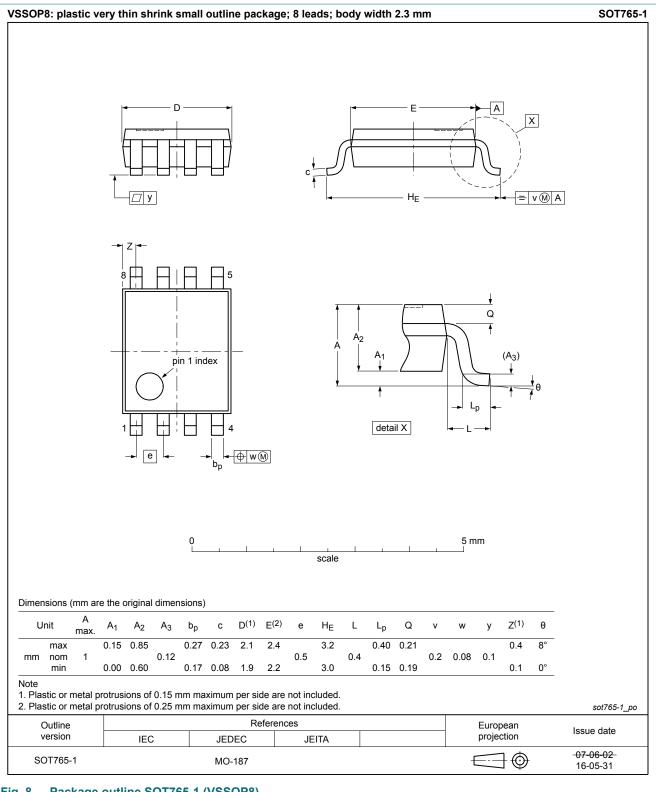


Fig. 8. Package outline SOT765-1 (VSSOP8)

### 13. Abbreviations

Acronym	Description
CMOS	Complementary Metal Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

### 14. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
74HC_HCT2G08 v.6	20181101	Product data sheet	-	74HC_HCT2G08 v.5		
Modifications:	Nexperia. <ul> <li>Legal texts has</li> </ul>	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Type numbers 74HC2G08GD and 74HCT2G08GD (SOT996-2/XSON8) removed.</li> </ul>				
74HC_HCT2G08 v.5	20131008	Product data sheet	-	74HC_HCT2G08 v.4		
Modifications:	• For type numbers 74HC2G08GD and 74HCT2G08GD XSON8U has changed to XSON8.					
74HC_HCT2G08 v.4	20090507	Product data sheet	-	74HC_HCT2G08 v.3		
74HC_HCT2G08 v.3	20031022	Product specification	-	74HC_HCT2G08 v.2		
74HC_HCT2G08 v.2	20030203	Product specification	-	74HC_HCT2G08 v.1		
74HC_HCT2G08 v.1	20020710	Product specification	-	-		

74HC\_HCT2G08

### 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

#### **Definitions**

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

#### **Disclaimers**

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal

#### **Dual 2-input AND gate**

injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <u>http://www.nexperia.com/profile/terms</u>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

#### Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

### Contents

1. General description	1
2. Features and benefits	1
3. Ordering information	1
4. Marking	1
5. Functional diagram	2
6. Pinning information	2
6.1. Pinning	2
6.2. Pin description	2
7. Functional description	3
8. Limiting values	3
9. Recommended operating conditions	3
10. Static characteristics	4
11. Dynamic characteristics	5
11.1. Waveforms and test circuit	6
12. Package outline	8
13. Abbreviations	10
14. Revision history	10
15. Legal information	11

© Nexperia B.V. 2018. All rights reserved

For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com Date of release: 1 November 2018

## **Mouser Electronics**

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Nexperia:

 74HC2G08DC,125
 74HC2G08DP,125
 74HCT2G08DC,125
 74HCT2G08DP,125
 74HC2G08GD,125

 74HCT2G08GD,125
 74HCT2G08GD,125
 74HCT2G08GD,125
 74HCT2G08GD,125
 74HCT2G08GD,125