# HEF4082B-Q100

# Dual 4-input AND gate Rev. 1 — 29 May 2015

**Product data sheet** 

#### **General description** 1.

The HEF4082B-Q100 is a dual 4-input AND gate. The outputs are fully buffered for highest noise immunity and pattern insensitivity to output impedance variations.

It operates over a recommended V<sub>DD</sub> power supply range of 3 V to 15 V referenced to V<sub>SS</sub> (usually ground). Unused inputs must be connected to  $V_{\text{DD}},\,V_{\text{SS}},$  or another input.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

#### **Features and benefits** 2.

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
  - ◆ Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Fully static operation
- 5 V, 10 V, and 15 V parametric ratings
- Standardized symmetrical output characteristics
- ESD protection:
  - MIL-STD-883, method 3015 exceeds 2000 V
  - ♦ HBM JESD22-A114F exceeds 2000 V
  - ightharpoonup MM JESD22-A115-A exceeds 200 V (C = 200 pF, R = 0 Ω)
- Inputs and outputs are protected against electrostatic effects
- Complies with JEDEC standard JESD 13-B

#### 3. **Ordering information**

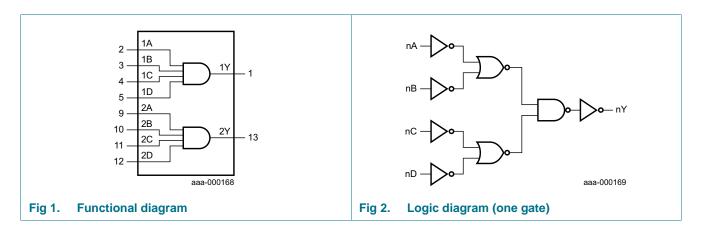
#### **Ordering information**

All types operate from  $-40 \,^{\circ}\text{C}$  to  $+125 \,^{\circ}\text{C}$ .

Type number	Package	ackage				
	Name	Description	Version			
HEF4082BT-Q100	SO14	plastic small outline package; 14 leads; body width 3.9 mm	SOT108-1			

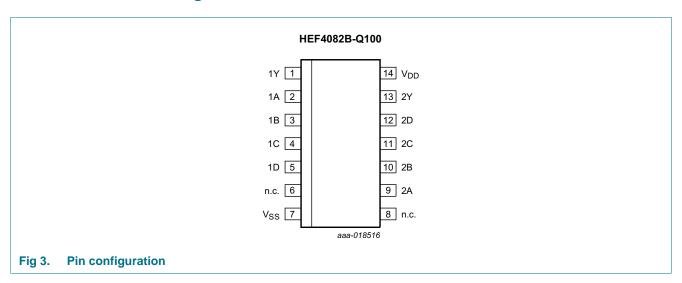


## 4. Functional diagram



## 5. Pinning information

## 5.1 Pinning



## 5.2 Pin description

Table 2. Pin description

Symbol	Pin	Description
1A, 1B, 1C, 1D	2, 3, 4, 5	input
2A, 2B, 2C, 2D	9, 10, 11, 12	input
1Y, 2Y	1, 13	output
n.c.	6, 8	not connected
V <sub>SS</sub>	7	ground (0 V)
$V_{DD}$	14	supply voltage

## 6. Functional description

Table 3. Function table[1]

Input	Output			
nA	nB	nC	nD	nY
L	X	Х	Х	L
X	L	Х	X	L
X	X	L	X	L
X	X	X	L	L
Н	Н	Н	Н	Н

<sup>[1]</sup> H = HIGH voltage level; L = LOW voltage level; X = don't care.

## 7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to  $V_{SS} = 0 \text{ V}$  (ground).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DD}$	supply voltage		-0.5	+18	V
I <sub>IK</sub>	input clamping current	$V_{I} < -0.5 \text{ V or } V_{I} > V_{DD} + 0.5 \text{ V}$	-	±10	mA
VI	input voltage		-0.5	V <sub>DD</sub> + 0.5	V
I <sub>OK</sub>	output clamping current	$V_{O} < -0.5 \text{ V or } V_{O} > V_{DD} + 0.5 \text{ V}$	-	±10	mA
I <sub>I/O</sub>	input/output current		-	±10	mA
I <sub>DD</sub>	supply current		-	50	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
T <sub>amb</sub>	ambient temperature		-40	+125	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40  ^{\circ}\text{C} \text{ to + } 125  ^{\circ}\text{C}$			
		SO14 [1	-	500	mW
Р	power dissipation	per output	-	100	mW

<sup>[1]</sup> For SO14 packages: above  $T_{amb}$  = 70 °C,  $P_{tot}$  derates linearly with 8 mW/K.

## 8. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DD}$	supply voltage		3	15	V
VI	input voltage		0	$V_{DD}$	V
T <sub>amb</sub>	ambient temperature	in free air	-40	+125	°C
Δt/ΔV	input transition rise and fall rate	$V_{DD} = 5 V$	-	3.75	ns/V
		V <sub>DD</sub> = 10 V	-	0.5	ns/V
		V <sub>DD</sub> = 15 V	-	0.08	ns/V

## 9. Static characteristics

Table 6. Static characteristics

 $V_{SS} = 0$  V;  $V_I = V_{SS}$  or  $V_{DD}$ ; unless otherwise specified.

Symbol	Parameter	Conditions	Conditions V <sub>DD</sub>		–40 °C	T <sub>amb</sub> =	+25 °C	T <sub>amb</sub> = +85 °C		T <sub>amb</sub> = +125 °C		Unit	
				Min	Max	Min	Max	Min	Max	Min	Max		
V <sub>IH</sub>	HIGH-level	$ I_{O}  < 1 \mu A$	5 V	3.5	-	3.5	-	3.5	-	3.5	-	V	
	input voltage		10 V	7.0	-	7.0	-	7.0	-	7.0	-	V	
			15 V	11.0	-	11.0	-	11.0	-	11.0	-	V	
V <sub>IL</sub>	LOW-level	$ I_{O}  < 1 \mu A$	5 V	-	1.5	-	1.5	-	1.5	-	1.5	V	
	input voltage		10 V	-	3.0	-	3.0	-	3.0	-	3.0	V	
			15 V	-	4.0	-	4.0	-	4.0	-	4.0	V	
V <sub>OH</sub>	HIGH-level	$ I_{O}  < 1 \mu A$	5 V	4.95	-	4.95	-	4.95	-	4.95	-	V	
	output voltage		10 V	9.95	-	9.95	-	9.95	-	9.95	-	V	
			15 V	14.95	-	14.95	-	14.95	-	14.95	-	V	
$V_{OL}$	LOW-level		$ I_{O}  < 1 \mu A$	5 V	-	0.05	-	0.05	-	0.05	-	0.05	V
	output voltage	voltage	10 V	-	0.05	-	0.05	-	0.05	-	0.05	V	
			15 V	-	0.05	-	0.05	-	0.05	-	0.05	V	
I <sub>OH</sub>	HIGH-level	V <sub>O</sub> = 2.5 V	5 V	-	-1.7	-	-1.4	-	-1.1	-	-1.1	mA	
	output current	V <sub>O</sub> = 4.6 V	5 V	-	-0.64	-	-0.5	-	-0.36	-	-0.36	mA	
		V <sub>O</sub> = 9.5 V	10 V	-	-1.6	-	-1.3	-	-0.9	-	-0.9	mA	
		V <sub>O</sub> = 13.5 V	15 V	-	-4.2	-	-3.4	-	-2.4	-	-2.4	mA	
I <sub>OL</sub>	LOW-level	V <sub>O</sub> = 0.4 V	5 V	0.64	-	0.5	-	0.36	-	0.36	-	mA	
	output current	$V_0 = 0.5 \text{ V}$	10 V	1.6	-	1.3	-	0.9	-	0.9	-	mA	
		V <sub>O</sub> = 1.5 V	15 V	4.2	-	3.4	-	2.4	-	2.4	-	mA	
I <sub>I</sub>	input leakage current		15 V	-	±0.1	-	±0.1	-	±1.0	-	±1.0	μА	
I <sub>DD</sub>	supply current	all valid input	5 V	-	0.25	-	0.25	-	7.5	-	7.5	μΑ	
		combinations;	10 V	-	0.5	-	0.5	-	15.0	-	15.0	μΑ	
		$I_O = 0 A$	15 V	-	1.0	-	1.0	-	30.0	-	30.0	μΑ	
Cı	input capacitance			-	-	-	7.5	-	-	-	-	pF	

## 10. Dynamic characteristics

Table 7. Dynamic characteristics

 $T_{amb}$  = 25 °C;  $C_L$  = 50 pF;  $t_r$  =  $t_f \le$  20 ns; waveforms see <u>Figure 4</u>; test circuit see <u>Figure 5</u>; unless otherwise specified. [1]

Symbol	Parameter	Conditions	$V_{DD}$	Extrapolation formula	Min	Тур	Max	Unit
t <sub>pd</sub>	propagation delay	nA, nB, nC,	5 V [2]	38 + 0.55 × C <sub>L</sub>	-	65	125	ns
		nD to nY	10 V	19 + 0.23 × C <sub>L</sub>	-	30	60	ns
			15 V	17 + 0.16 × C <sub>L</sub>	-	25	45	ns
t <sub>THL</sub>	HIGH to LOW output	nY	5 V	10 + 1.0 × C <sub>L</sub>	-	60	120	ns
	transition time		10 V	9 + 0.42 × C <sub>L</sub>	-	30	60	ns
			15 V	6 + 0.28 × C <sub>L</sub>	-	20	40	ns
t <sub>TLH</sub>	LOW to HIGH output	nY	5 V	10 + 1.0 × C <sub>L</sub>	-	60	120	ns
	transition time	ansition time 10 V	10 V	9 + 0.42 × C <sub>L</sub>	-	30	60	ns
			15 V	6 + 0.28 × C <sub>L</sub>	-	20	40	ns

<sup>[1]</sup> The typical value of the propagation delay and output transition time can be calculated with the extrapolation formula (C<sub>L</sub> in pF).

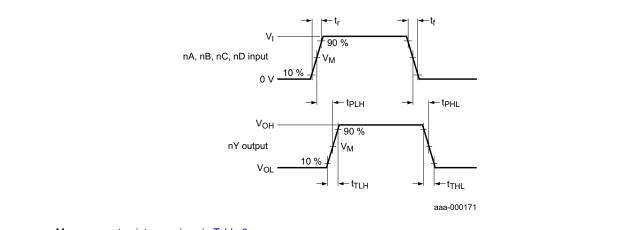
Table 8. Dynamic power dissipation

 $V_{SS} = 0 \text{ V; } t_r = t_f \le 20 \text{ ns; } T_{amb} = 25 \text{ °C.}$ 

Symbol	Parameter	$V_{DD}$	Typical formula	where:
$P_D$	dynamic power dissipation	5 V	$P_D = 1500 \times f_i + \Sigma(f_o \times C_L) \times V_{DD}^2 (\mu W)$	$f_i$ = input frequency in MHz;
		10 V	$P_D = 6700 \times f_i + \Sigma (f_o \times C_L) \times V_{DD}^2 (\mu W)$	f <sub>o</sub> = output frequency in MHz;
		15 V	$P_D = 16800 \times f_i + \Sigma (f_o \times C_L) \times V_{DD}^2 (\mu W)$	$C_L$ = output load capacitance in pF;
				$\Sigma(f_0 \times C_L)$ = sum of the outputs;
				$V_{DD}$ = supply voltage in V.

<sup>[2]</sup>  $t_{pd}$  is the same as  $t_{PHL}$  and  $t_{PLH}$ .

### 11. Waveforms



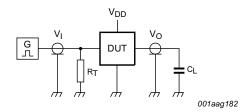
Measurement points are given in Table 9.

Logic levels:  $V_{\text{OL}}$  and  $V_{\text{OH}}$  are typical output voltage levels that occur with the output load.

Fig 4. Input to output propagation delay and output transition times

Table 9. Measurement points

Supply voltage	Input	Output
$V_{DD}$	V <sub>M</sub>	V <sub>M</sub>
5 V to 15 V	0.5V <sub>DD</sub>	0.5V <sub>DD</sub>



Test data is given in Table 10.

Definitions for test circuit:

DUT = Device Under Test.

 $C_L$  = load capacitance including jig and probe capacitance.

 $R_T$  = termination resistance should be equal to the output impedance  $Z_0$  of the pulse generator.

Fig 5. Test circuit for measuring switching times

Table 10. Test data

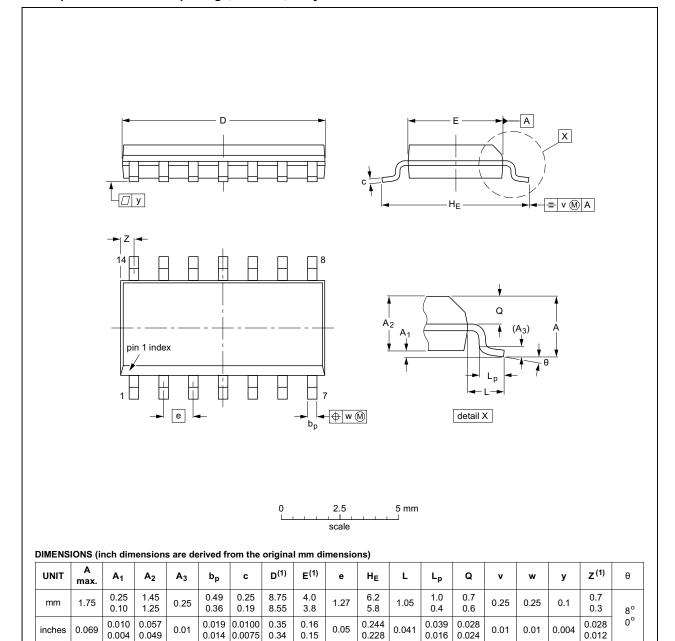
Supply voltage	Input	Load	
$V_{DD}$	VI	t <sub>r</sub> , t <sub>f</sub>	CL
5 V to 15 V	V <sub>SS</sub> or V <sub>DD</sub>	≤ 20 ns	50 pF

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## 12. Package outline

#### SO14: plastic small outline package; 14 leads; body width 3.9 mm

SOT108-1



#### Note

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

OUTLINE REFERENCES					EUROPEAN	ICCUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT108-1	076E06	MS-012				<del>99-12-27</del> 03-02-19	

Fig 6. Package outline SOT108-1 (SO14)

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## 13. Abbreviations

#### Table 11. Abbreviations

Acronym	Description
HBM	Human Body Model
ESD	ElectroStatic Discharge
MM	Machine Model
MIL	Military

## 14. Revision history

#### Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
HEF4082B_Q100 v.1	20150529	Product data sheet	-	-

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Document status[1][2]	Product status[3]	Definition
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