

NX138AK 60 V, N-channel Tench MOSFET 10 June 2016

Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection

3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	60	V
V _{GS}	gate-source voltage			-20	-	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	-	190	mA
Static characteristics							
R _{DSon}	drain-source on-state resistance	V_{GS} = 10 V; I _D = 190 mA; T _j = 25 °C		-	3	4.5	Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm².

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5. Pinning information

Table 2. F	Pinning inf	formation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D
2	S	source		
3	D	drain	1 2 TO-236AB (SOT23)	G G S 017aaa255

6. Ordering information

Table 3. Ordering information							
Type number	Package						
	Name	Description	Version				
NX138AK	TO-236AB	plastic surface-mounted package; 3 leads	SOT23				

7. Marking

Table 4. Marking codes	
Type number	Marking code[1]
NX138AK	AP%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	60	V
V _{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	190	mA
		V _{GS} = 10 V; T _{amb} = 100 °C	[1]	-	120	mA
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	765	mA
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	265	mW
			[1]	-	325	mW
		T _{sp} = 25 °C		-	1.33	W
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drain	n diode	•		·		
I _S	source current	T _{amb} = 25 °C	[1]	-	190	mA

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

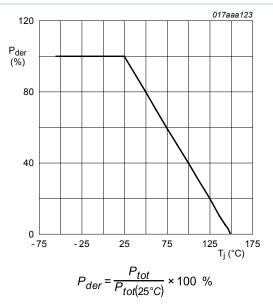
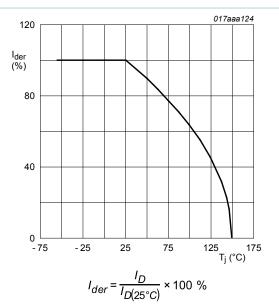


Fig. 1. Normalized total power dissipation as a function of junction temperature





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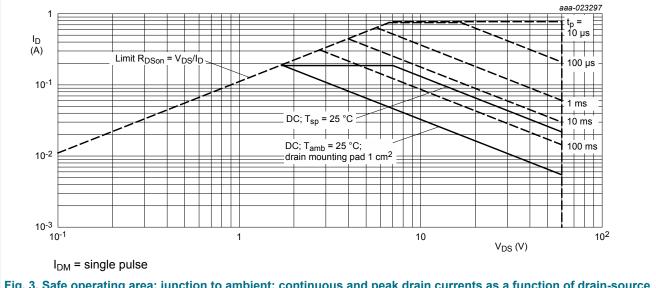


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

9. Thermal characteristics

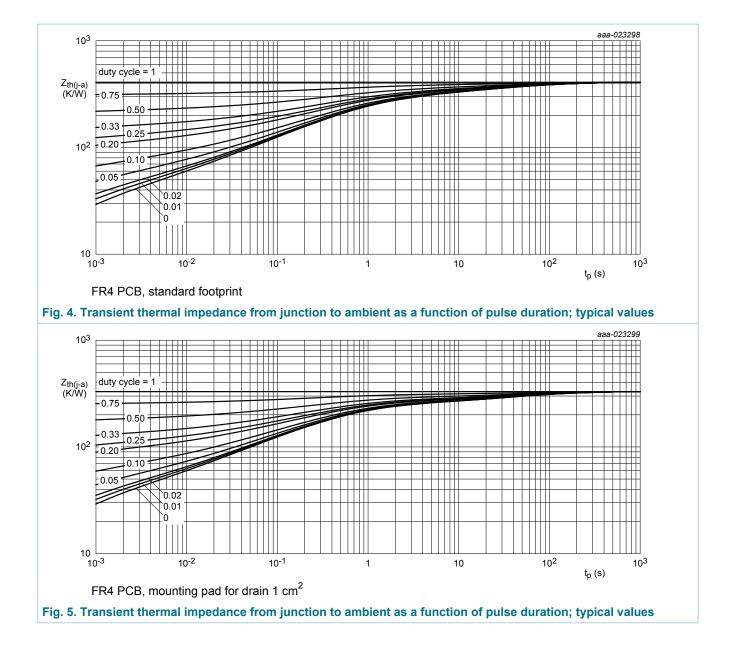
Table 6. The	rmal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)}	thermal resistance	in free air	[1]	-	410	470	K/W
	from junction to ambient		[2]	-	330	380	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	80	95	K/W

[1]

Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint. Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm². [2]

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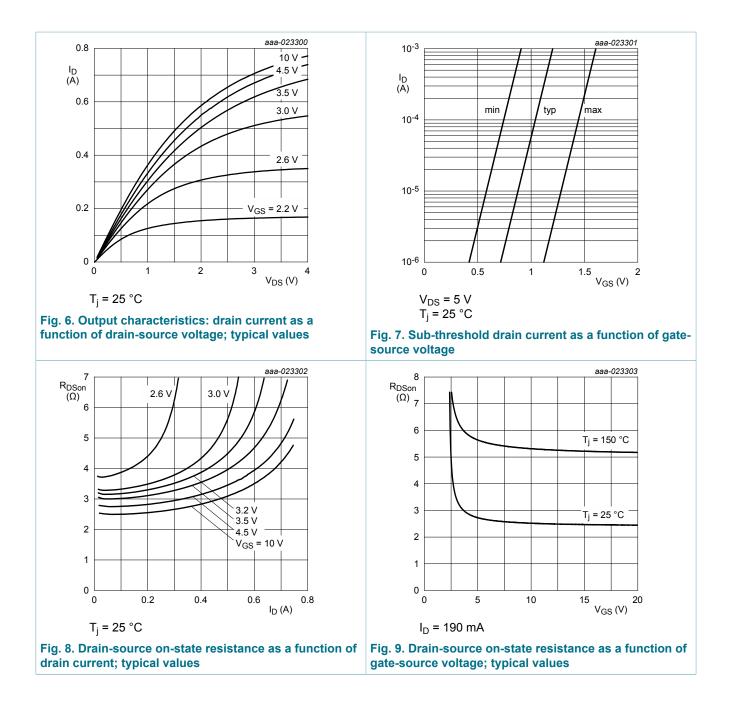


10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _j = 25 °C	60	-	-	V
V _{GSth}	gate-source threshold voltage	$I_D = 250 \ \mu\text{A}; \ V_{DS} = V_{GS}; \ T_j = 25 \ ^{\circ}\text{C}$	0.8	1.1	1.5	V
I _{DSS}	drain leakage current	V _{DS} = 60 V; V _{GS} = 0 V; T _j = 25 °C	-	-	1	μA
I _{GSS}	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	2	μA
		V_{GS} = -20 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-2	μA
		V _{GS} = 10 V; V _{DS} = 0 V; T _j = 25 °C	-	-	0.5	μA
		V_{GS} = -10 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-0.5	μA
		V_{GS} = 5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	100	nA
		V_{GS} = -5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-100	nA
R _{DSon} drain-source on-state resistance	drain-source on-state	V _{GS} = 10 V; I _D = 190 mA; T _j = 25 °C	-	3	4.5	Ω
	resistance	V _{GS} = 10 V; I _D = 190 mA; T _j = 150 °C	-	6	9	Ω
		V _{GS} = 5 V; I _D = 170 mA; T _j = 25 °C	-	4	5.2	Ω
		V_{GS} = 2.5 V; I _D = 130 mA; T _j = 25 °C	-	5	10	Ω
9 _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 180 mA; T _j = 25 °C	-	3.5	-	S
Dynamic ch	aracteristics					
Q _{G(tot)}	total gate charge	V_{DS} = 30 V; I _D = 190 mA; V _{GS} = 10 V;	-	0.9	1.4	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.1	-	nC
Q _{GD}	gate-drain charge		-	0.2	-	nC
C _{iss}	input capacitance	V _{DS} = 30 V; f = 1 MHz; V _{GS} = 0 V;	-	15	20	pF
C _{oss}	output capacitance	T _j = 25 °C	-	2.3	-	pF
C _{rss}	reverse transfer capacitance		-	1.5	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 30 V; I _D = 190 mA; V _{GS} = 10 V;	-	8	12	ns
t _r	rise time	R _{G(ext)} = 75 Ω; T _j = 25 °C	-	10	-	ns
t _{d(off)}	turn-off delay time		-	8	20	ns
t _f	fall time		-	5	-	ns
Source-drai	n diode	· · ·	1	1	1	
V _{SD}	source-drain voltage	I _S = 190 mA; V _{GS} = 0 V; T _i = 25 °C	-	0.8	1.2	V

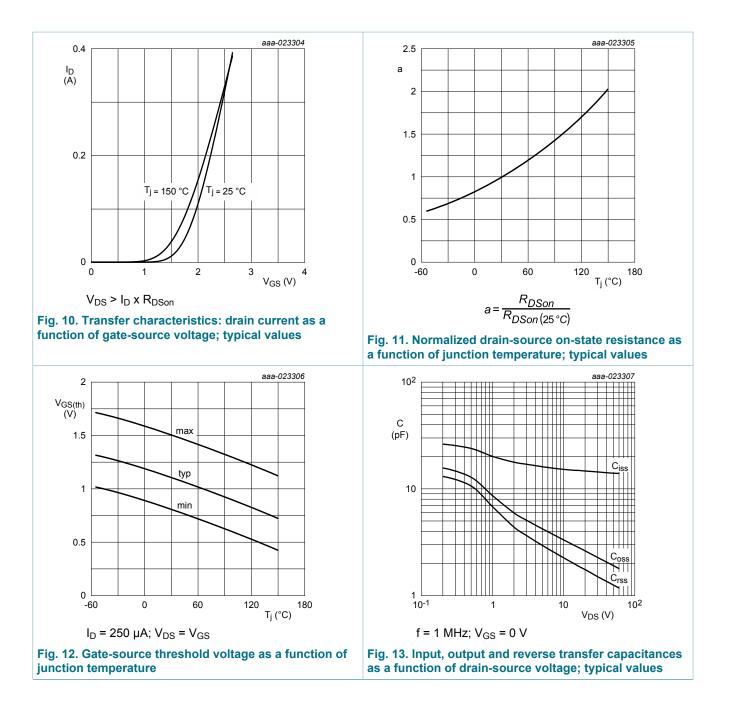
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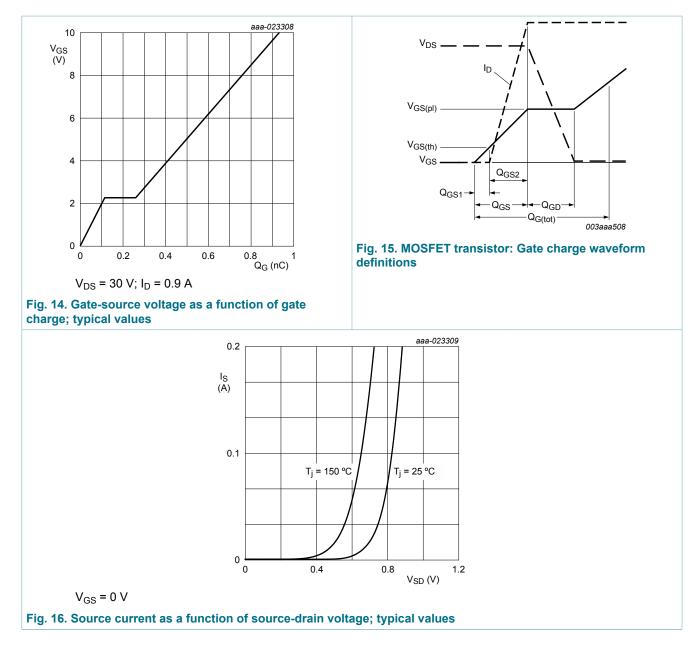
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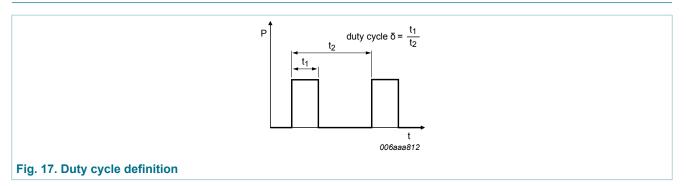
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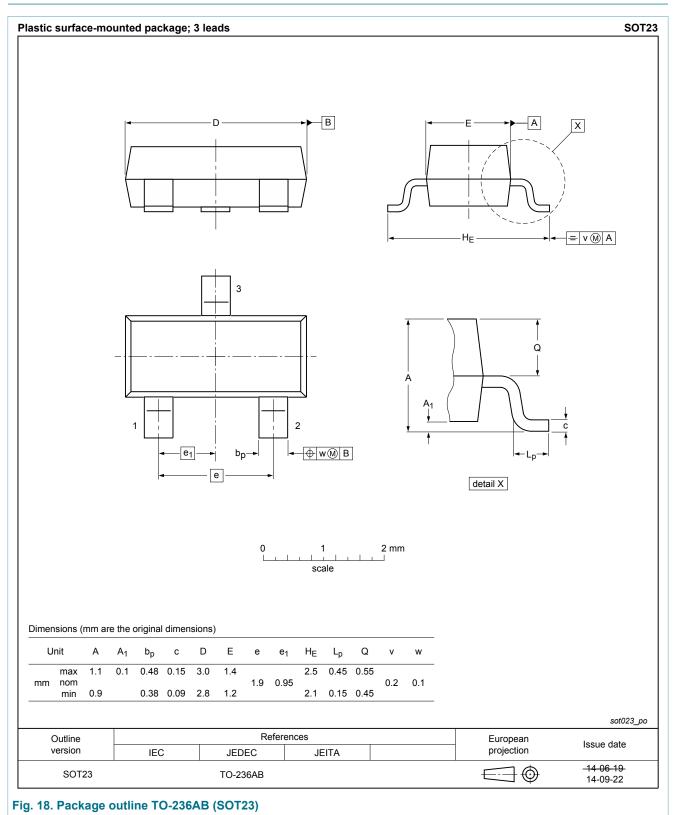


11. Test information



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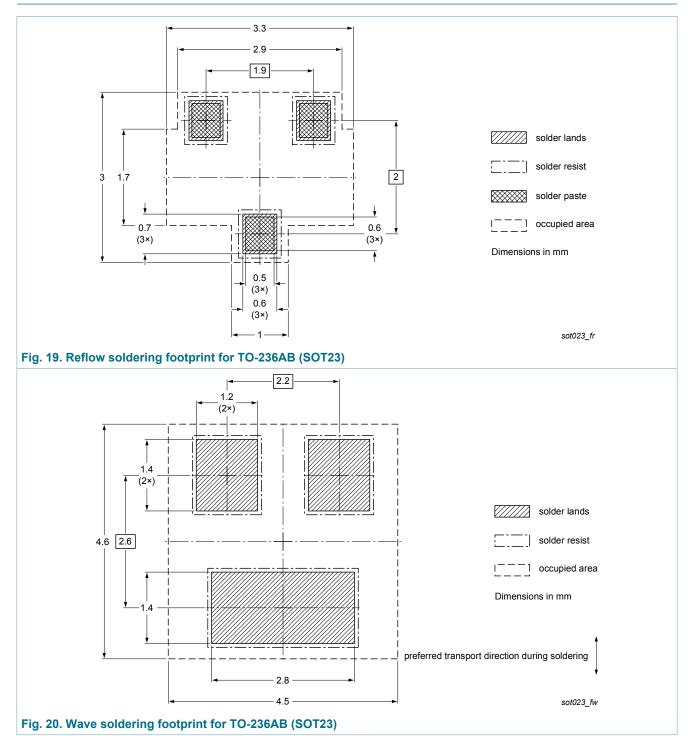
12. Package outline



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13. Soldering



14. Revision history

Table 8. Revision history								
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
NX138AK v.2	20160610	Product data sheet	-	NX138AK v.1				
Modifications:	Title changed for	Title changed for figures 11 and 12 to juction temperature						
NX138AK v.1	20160607	Product data sheet	-	-				

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

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