TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX86F, TC74LCX86FK

Low-Voltage Quad 2-Input Exclusive OR Gate with 5-V Tolerant Inputs and Outputs

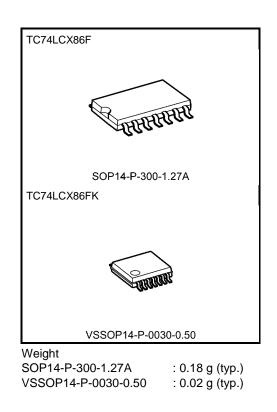
The TC74LCX86 is a high-performance CMOS exclusive OR gate. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage (3.3 V) V_{CC} applications, but it could be used to interface to 5-V supply environment for inputs.

All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: VCC = 1.65 to 3.6 V
- High-speed operation: $t_{pd} = 6.5 \text{ ns} (\text{max}) (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Output current: $|I_{OH}|/I_{OL} = 24 \text{ mA} (\text{min}) (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: >±500 mA
- Available in JEITA SOP, VSSOP (US)
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 86 type

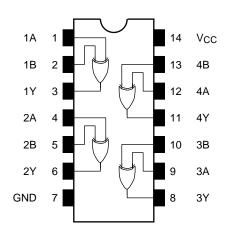


Note: The Electrical Characteristics of V_{CC} = 1.8 ± 0.15 V is only applicable for products which manufactured from January 2009 onward.

Start of commercial production 1995-02

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Pin Assignment (top view)



IEC Logic Symbol

Truth Table

А	В	Y
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	Vcc	-0.5 to 7.0	V	
DC input voltage	Vin	-0.5 to 7.0	V	
		-0.5 to 7.0 (Note 2)		
DC output voltage	Vout	-0.5 to V _{CC} + 0.5 (Note 3)	V	
Input diode current	lıк	-50	mA	
Output diode current	IOK	±50 (Note 4)	mA	
DC output current	IOUT	±50	mA	
Power dissipation	PD	180	mW	
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA	
Storage temperature	T _{stg}	-65 to 150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: VCC = 0 V

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: VOUT < GND, VOUT > VCC

Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit	
Dowor ownoly voltage	Vee	1.65 to 3.6	V	
Power supply voltage	Vcc	1.5 to 3.6 (Note 2)	v	
Input voltage	VIN	0 to 5.5	V	
		0 to 5.5 (Note 3)	V	
Output voltage	Vout	0 to V _{CC} (Note 4)		
Output ourroat	Іон/Іог	±24 (Note 5)	~	
Output current	IOH/IOL	±12 (Note 6)	mA	
Operating temperature	Topr	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V	

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Note 2: Data retention only

Note 3: VCC = 0 V

Note 4: High or low state

Note 5: VCC = 3.0 to 3.6 V

Note 6: VCC = 2.7 to 3.0 V

Note 7: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Characteris	stics	Symbol	mbol Test Condition Vcc (V)		Vcc (V)	Min	Max	Unit
					1.65 to 2.3	V _{CC} × 0.9		
	H-level	Vih			2.3 to 2.7	1.7		
						2.0		V
Input voltage						i5 to 2.3 —	V _{CC} × 0.1	v
	L-level	VIL	_		2.3 to 2.7		0.7	
					2.7 to 3.6		0.8	
				$I_{OH} = -100 \ \mu A$	1.65 to 3.6	V _{CC} -0.2	_	
			H VIN = VIH or VIL	Ioн = -4 mA	1.65	1.05		
	H-level	Vон		Iон = -8 mA	2.3	1.7	_	V
				I _{OH} = -12 mA	2.7	2.2	_	
				Iон = -18 mA	3.0	2.4	_	
				Iон = -24 mA	3.0	2.2		
Output voltage				I _{OL} = 100 μA	1.65 to 3.6	_	0.2	
				IoL = 4 mA	1.65	_	0.45	
	Linut			IOL = 8 mA	2.3	_	0.7	
	L-level	Vol	VIN = VIH or VIL	I _{OL} = 12 mA	2.7	_	0.4	
			I _{OL} = 16 mA	3.0	_	0.4		
				I _{OL} = 24 mA	3.0	_	0.55	
Input leakage currer	nt	lin	V _{IN} = 0 to 5.5 V		1.65 to 3.6	_	±5.0	μA
Power-off leakage c	urrent	IOFF	VIN/VOUT = 5.5 V		0		10.0	μΑ
		laa	$V_{IN} = V_{CC} \text{ or } GND$		1.65 to 3.6		10.0	
Quiescent supply cu	litent	ICC	$V_{IN} = 3.6$ to 5.5 V		1.65 to 3.6		±10.0	μΑ
Increase in ICC per	input	∆lcc	VIH = VCC - 0.6 V (per 1	input)	2.7 to 3.6		500	

AC Characteristics (Ta = -40 to 85° C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
			1.8 ± 0.15		35.0	ns
Dropogation dology time	tpLH tpHL	Figure 1, Figure 2	2.5 ± 0.2		10.0	
Propagation delay time			2.7		7.0	
			$\textbf{3.3}\pm\textbf{0.3}$	1.5	6.5	
	tosLH tosHL	(81-1-)	2.7			20
Output to output skew		(Note)	$\textbf{3.3}\pm\textbf{0.3}$		1.0	ns

Note: Parameter guaranteed by design.

(tosLH = |tpLHm - tpLHn|, tosHL = |tpHLm - tpHLn|)

Dynamic Switching Characteristics (Ta = 25°C, input: tr = tf = 2.5 ns, CL = 50 pF, RL = 500 Ω)

Characteristics	Symbol	Test Condition	Vcc (V)	Тур.	Unit
Quiet output maximum dynamic VOL	Volp	$V_{IH} = 3.3 \text{ V}, \text{ VIL} = 0 \text{ V}$	3.3	0.8	V
Quiet output minimum dynamic VOL	Volv	$V_{IH}=3.3~V,~V_{IL}=0~V$	3.3	0.8	V

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	CIN	—	3.3	7	pF
Output capacitance	COUT	—	0	8	pF
Power dissipation capacitance	Cpd	f _{IN} = 10 MHz (Note) 3.3	25	pF

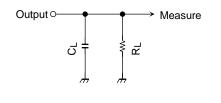
Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

ICC (opr) = CPD \cdot VCC \cdot fIN + ICC/4 (per gate)

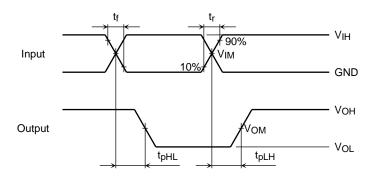
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AC Test Circuit





AC Waveform





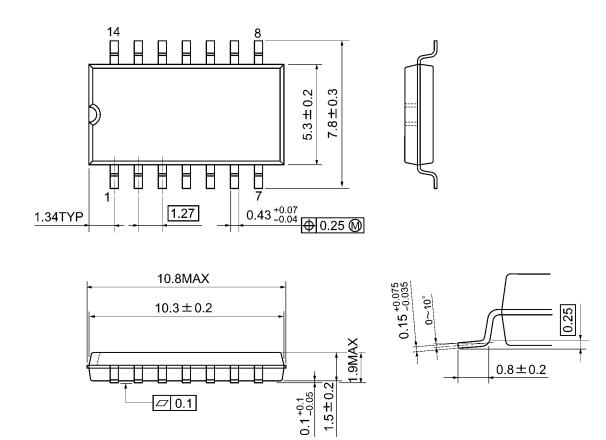
			Vcc	
	Symbol	3.3 ± 0.3 V 2.7 V	$2.5\pm0.2~V$	$1.8\pm0.15\;V$
Input	VIH	2.7 V	Vcc	Vcc
	VIM	1.5 V	V _{CC} /2	V _{CC} /2
	t _r , t _f	2.5 ns	2.0 ns	2.0 ns
Output	VOM	1.5 V	V _{OH} /2	V _{OH} /2
Load	CL	50 pF	30 pF	30 pF
	RL	500 Ω	500 Ω	1 kΩ



Package Dimensions

SOP14-P-300-1.27A

Unit: mm



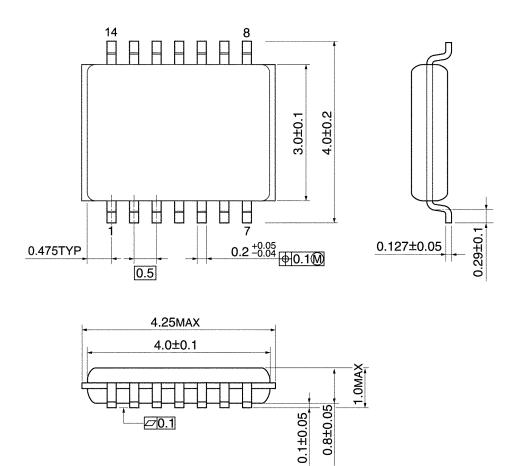
Weight: 0.18 g (typ.)



Package Dimensions

VSSOP14-P-0030-0.50

Unit: mm



Weight: 0.02 g (typ.)

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