TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX574F, TC74LCX574FK

Low-Voltage Octal D-Type Flip-Flop with 5-V Tolerant Inputs and Outputs

The TC74LCX574 is a high-performance CMOS octal D-type flip-flop. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

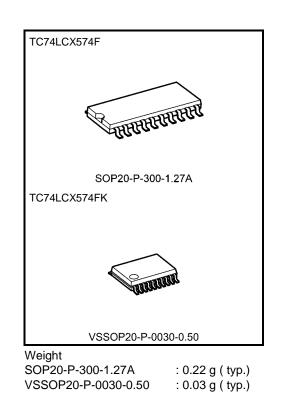
The device is designed for low-voltage $(3.3 \text{ V}) \text{ V}_{CC}$ applications, but it could be used to interface to 5 V supply environment for both inputs and outputs.

This 8-bit D-type flip-flop is controlled by a clock input (CK) and an output enable input (\overline{OE}). When the \overline{OE} input is high, the eight outputs are in a high-impedance state.

All inputs are equipped with protection circuits against static discharge.

Features

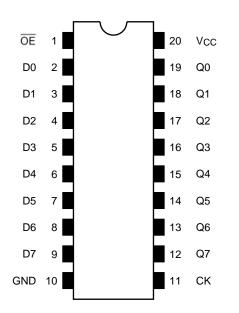
- Low-voltage operation: VCC = 1.65 to 3.6 V
- High-speed operation: $t_{pd} = 8.5 \text{ ns} (max) (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Output current: $|I_{OH}|/I_{OL} = 24 \text{ mA} (\text{min}) (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: >±500 mA
- Available in JEITA SOP, VSSOP (US)
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 574 type



Note: The Electrical Characteristics of V_{CC} = 1.8 ± 0.15 V is only applicable for products which manufactured from January 2009 onward.

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Pin Assignment (top view)



Truth Table

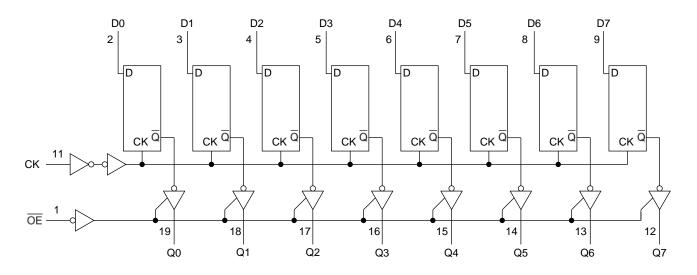
	Quitouito		
ŌĒ	СК	D	Outputs
Н	Х	Х	Z
L		Х	Qn
L		L	L
L		Н	Н

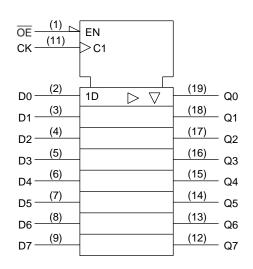
X: Don't care

Z: High impedance

Qn: No change

System Diagram





Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	Vcc	-0.5 to 7.0	V
DC input voltage	Vin	-0.5 to 7.0	V
		-0.5 to 7.0 (Note 2)	V
DC output voltage	Vout	-0.5 to V _{CC} + 0.5 (Note 3)	
Input diode current	liк	-50	mA
Output diode current	lok	±50 (Note 4)	mA
DC output current	lout	±50	mA
Power dissipation	PD	180	mW
DC V _{CC} /ground current	ICC/IGND	±100	mA
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 2: Output in OFF state
- Note 3: High or low state. IOUT absolute maximum rating must be observed.
- Note 4: VOUT < GND, VOUT > VCC

Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit
Dower oupply yelfore		1.65 to 3.6	V
Power supply voltage	Vcc	1.5 to 3.6 (Note 2)	v
Input voltage	VIN	0 to 5.5	V
	Vout	0 to 5.5 (Note 3)	V
Output voltage	VOUT	0 to V _{CC} (Note 4)	
Output current	IOH/IOL	±24 (Note 5)	mA
		±12 (Note 6)	IIIA
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Note 2: Data retention only

- Note 3: Output in OFF state
- Note 4: High or low state

Note 5: $V_{CC} = 3.0$ to 3.6 V

- Note 6: VCC = 2.7 to 3.0 V
- Note 7: VIN = 0.8 to 2.0 V, VCC = 3.0 V

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Characteris	stics	Symbol	Test Co	Test Condition Vcc (V)			Max	Unit			
					1.65 to 2.3	V _{CC} × 0.9					
H-level		VIH	_		2.3 to 2.7	1.7					
					2.7 to 3.6	2.0		V			
Input voltage					1.65 to 2.3		V _{CC} × 0.1				
	L-level	VIL	-	-	2.3 to 2.7		0.7				
					2.7 to 3.6		0.8				
				$I_{OH} = -100 \ \mu A$	1.65 to 3.6	Vcc-0.2	_				
				$I_{OH} = -4 \text{ mA}$	1.65	1.05	_				
	H-level	Vон	VIN = VIH or VIL	IOH = -8 mA	2.3	1.7	_				
		VOH		$I_{OH} = -12 \text{ mA}$	2.7	2.2	_				
				I _{OH} = -18 mA	3.0	2.4	_				
Output voltage				Ioh = -24 mA	3.0	2.2	_				
Output voltage			$V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 100 \ \mu A$	1.65 to 3.6	_	0.2				
				$I_{OL} = 4 \text{ mA}$	1.65	—	0.45				
		Mai		IOL = 8 mA	2.3	—	0.7				
	L-level	Vol		Iol = 12 mA	2.7	—	0.4				
				I _{OL} = 16 mA	3.0	—	0.4				
							IoL = 24 mA	3.0	—	0.55	
Input leakage current		I _{IN}	V _{IN} = 0 to 5.5 V		1.65 to 3.6	—	±5.0	μA			
3-state output off-state	-state output off-state current IOZ VIN = VIH or VIL VOUT = 0 to 5.5 V		1.65 to 3.6		±5.0	μA					
Power off leakage curre	ower off leakage current		$V_{IN}/V_{OUT} = 5.5 V$		0		10.0	μΑ			
	t		VIN = VCC or GND		1.65 to 3.6		10.0				
Quiescent supply curre		ICC	V _{IN} /V _{OUT} = 3.6 to 5.5 V		1.65 to 3.6		±10.0	μΑ			
Increase in I _{CC} per inp	ut	Δlcc	VIH = ACC - 0.6	V (per 1 input)	2.7 to 3.6		500				

AC Characteristics (Ta = -40 to 85° C)

Characteristics	Symbol	Test Condition		C (V) Min Max		Unit
Characteristics	Symbol		V _{CC} (V)			
			1.8 ± 0.15	50		MHz
	4		2.5 ± 0.2	100		
Maximum clock frequency	f _{max}	Figure 1, Figure 2	2.7	100	_	
			$\textbf{3.3}\pm\textbf{0.3}$	150		
			1.8 ± 0.15		30.0	
Propagation delay time	tpLH	Figure 1, Figure 2	2.5 ± 0.2	_	10.5	20
(CK-Q)	tpHL		2.7		9.5	ns
			$\textbf{3.3}\pm\textbf{0.3}$	1.5	8.5	
			1.8 ± 0.15		34.0	
Output an able time	tpZL		2.5 ± 0.2		17.0	
Output enable time	tpZH	Figure 1, Figure 3	2.7	_	9.5	ns
			$\textbf{3.3}\pm\textbf{0.3}$	1.5	8.5	
		Figure 1, Figure 3	1.8 ± 0.15	_	28.0	ns
	t _{pLZ}		2.5 ± 0.2		14.0	
Output disable time	t _{pHZ}		2.7		7.0	
			$\textbf{3.3}\pm\textbf{0.3}$	1.5	6.5	
		Figure 1, Figure 2	1.8 ± 0.15	10.0	—	ns
Minimum pulse width	t _w (H)		2.5 ± 0.2	5.0		
(CK)	t _w (L)		2.7	3.3	_	
			$\textbf{3.3}\pm\textbf{0.3}$	3.3	—	
			1.8 ± 0.15	10.0		ns
Minimum and up time			2.5 ± 0.2	5.0	_	
Minimum set-up time	ts	Figure 1, Figure 2	2.7	2.5	_	
			$\textbf{3.3}\pm\textbf{0.3}$	2.5		
Minimum hold time			1.8 ± 0.15	1.5		
	+ .	Figure 1 Figure 2	2.5 ± 0.2	1.5		
	th	Figure 1, Figure 2	2.7	1.5	—	ns
			$\textbf{3.3}\pm\textbf{0.3}$	1.5	_	
Output to output skew	t _{osLH}	(Note)	2.7			ne
	t _{osHL}	(Note)	$\textbf{3.3}\pm\textbf{0.3}$	_	1.0	ns

Note: Parameter guaranteed by design.

(tosLH = |tpLHm - tpLHn|, tosHL = |tpHLm - tpHLn|)

Dynamic Switching Characteristics (Ta= 25°C, input: $t_r = t_f = 2.5 \text{ ns}$, CL= 50 pF, RL= 500 Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V_{OL}	VOLP	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V
Quiet output minimum dynamic V_{OL}	Volv	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V

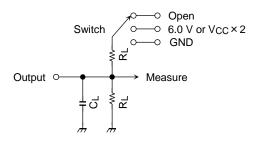
Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	CIN	_	3.3	7	pF
Output capacitance	Соит	—	3.3	8	pF
Power dissipation capacitance	Cpd	f _{IN} = 10 MHz (Note)	3.3	25	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption.

Average operating current can be obtained by the equation: $ICC (opr) = CPD \cdot VCC \cdot fIN + ICC/8$ (per bit)

AC Test Circuit



Parameter	Switch		
t _{pLH} , t _{pHL}	Open		
tpLZ, tpZL	6.0 V	@ V _{CC} =3.3±0.3V @ V _{CC} =2.7V	
	V _{CC} ×2	@ V _{CC} =2.5±0.2V @ V _{CC} =1.8±0.15V	
t _{pHZ} , t _{pZH}	GND		

Figure 1

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AC Waveform

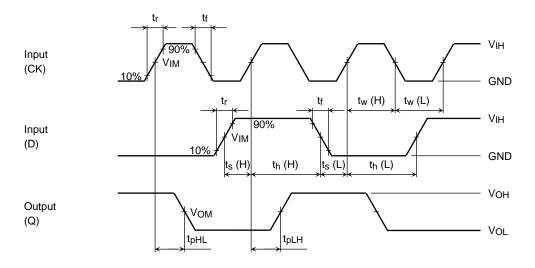


Figure 2 t_{pLH}, t_{pHL}, t_w, t_s, t_h

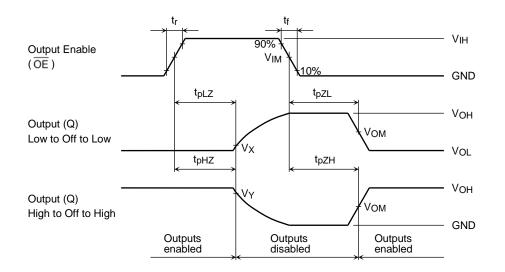


Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

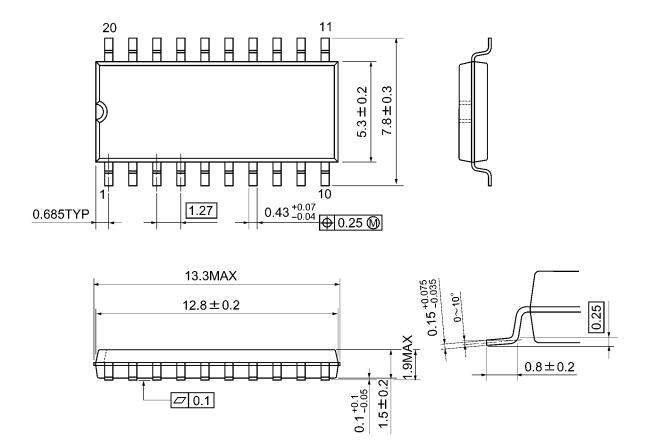
		Vcc			
	Symbol	3.3 ± 0.3 V 2.7 V	$2.5\pm0.2\;\text{V}$	$1.8\pm0.15~\text{V}$	
Input	VIH	2.7 V	V _{CC}	V _{CC}	
	VIM	1.5 V	V _{CC} /2	V _{CC} /2	
	t _r , t _f	2.5 ns	2.0 ns	2.0 ns	
Output	Vom	1.5 V	V _{OH} /2	V _{OH} /2	
	Vx	V _{OL} +0.3 V	V _{OL} +0.15 V	V _{OL} +0.15 V	
	Vy	V _{OH} -0.3 V	V _{OH} -0.15 V	V _{OH} -0.15 V	
Load	CL	50 pF	30 pF	30 pF	
	RL	500 Ω	500 Ω	1 kΩ	



Package Dimensions

SOP20-P-300-1.27A

Unit: mm



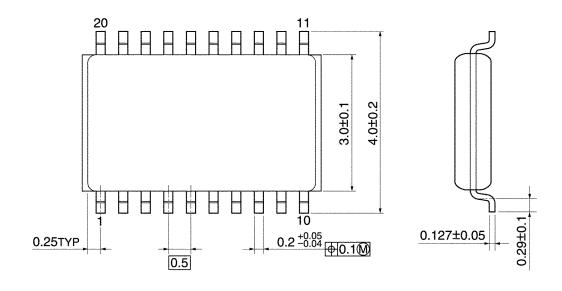
Weight: 0.22 g (typ.)

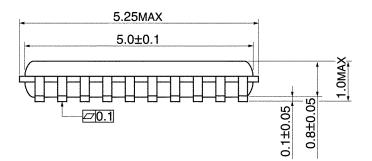


Package Dimensions

VSSOP20-P-0030-0.50

Unit: mm





Weight: 0.03 g (typ.)

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