TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHC21F, TC74VHC21FK

Dual 4-Input AND Gate

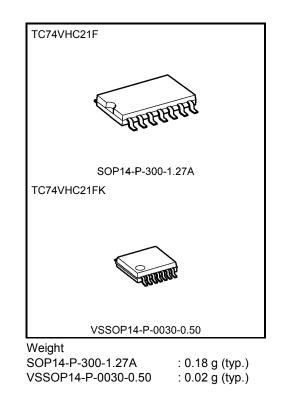
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The TC74VHC21 is an advanced high speed CMOS 4-INPUT AND GATE fabricated with silicon gate $\rm C^2MOS$ technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The internal circuit is composed of 4 stages including a buffer output, which provide high noise immunity and stable output.

An input protection circuit ensures that 0 to 5.5 V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5 V to 3 V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

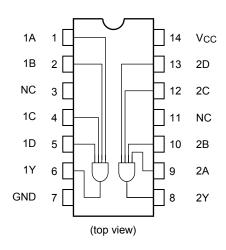


Features

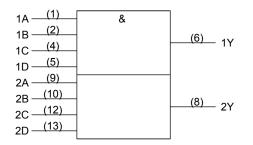
- High speed: $t_{pd} = 3.3$ ns (typ.) at VCC = 5 V
- Low power dissipation: $I_{CC} = 2 \mu A \pmod{at Ta} = 25 \circ C$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Power down protection is provided on all inputs.
- Balanced propagation delays: $t_{pLH} \approx t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2 to 5.5 V
- Pin and function compatible with 74ALS21

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Pin Assignment



IEC Logic Symbol



Truth Table

	Inp	Output			
А	В	С	D	Y	
L	Х	Х	Х	L	
Х	L	Х	Х	L	
х	Х	L	Х	L	
Х	Х	Х	L	L	
Н	Н	Н	Н	Н	

X: Don't care

Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	-0.5 to 7.0	V
DC input voltage	Vin	-0.5 to 7.0	V
DC output voltage	Vout	-0.5 to V _{CC} + 0.5	V
Input diode current	liк	-20	mA
Output diode current	Іок	±20	mA
DC output current	Ιουτ	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2.0 to 5.5	V
Input voltage	VIN	0 to 5.5	V
Output voltage	Vout	0 to Vcc	V
Operating temperature	Topr	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 (V _{CC} = 3.3 ± 0.3 V) 0 to 20 (V _{CC} = 5 ± 0.5 V)	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit	
	- ,			V _{CC} (V)	Min	Тур.	Max	Min	Max	•••••
High-level input voltage	Vih	_		2.0 3.0 to 5.5	1.50 V _{CC} × 0.7		_	1.50 V _{CC} × 0.7		V
Low-level input voltage	VIL	_		2.0 3.0 to 5.5			0.50 V _{CC} × 0.3		0.50 V _{CC} × 0.3	V
High-level output voltage	Vон	VIN = VIH	I _{OH} = -50 μA I _{OH} = -4 mA	2.0 3.0 4.5 3.0	1.9 2.9 4.4 2.58	2.0 3.0 4.5		1.9 2.9 4.4 2.48		V
			$I_{OH} = -8 \text{ mA}$	4.5	3.94	_	_	3.80	_	
Low-level output voltage	Vol	VIN = VIH or VIL	I _{OL} = 50 μA	2.0 3.0 4.5		0.0 0.0 0.0	0.1 0.1 0.1		0.1 0.1 0.1	V
			I _{OL} = 4 mA I _{OL} = 8 mA	3.0 4.5			0.36 0.36	_	0.44 0.44	
Input leakage current	I _{IN}	$V_{IN} = 5.5 V \text{ or GND}$		0 to 5.5	_	_	±0.1	_	±1.0	μA
Quiescent supply current	ICC	$V_{IN} = V_{CC} \text{ or } GND$		5.5	_		2.0	_	20.0	μA



AC Characteristics (input: $t_r = t_f = 3 ns$)

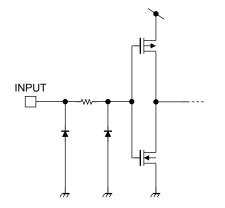
Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit	
			Vcc (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	t _{pLH} t _{pHL}	_	3.3 ± 0.3	15	_	4.8	7.0	1.0	8.5	ns
				50	_	7.3	10.5	1.0	12.0	
			5.0 ± 0.5	15	_	3.3	5.0	1.0	6.0	
				50	_	4.8	7.0	1.0	8.0	
Input capacitance	CIN		_		_	4	10	_	10	pF
Power dissipation capacitance	C _{PD}			(Note)	_	20	—	_	_	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

ICC (opr) = CPD·VCC·fIN + ICC/2 (per gate)

Input Equivalent Circuit

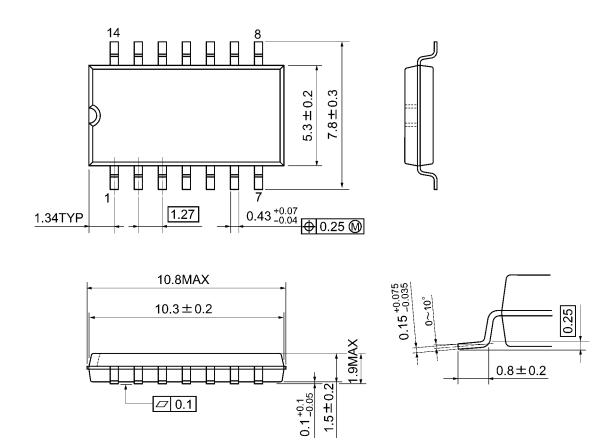




Package Dimensions

SOP14-P-300-1.27A

Unit: mm



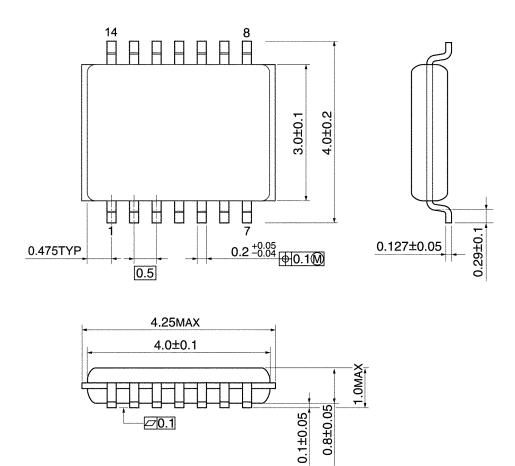
Weight: 0.18 g (typ.)



Package Dimensions

VSSOP14-P-0030-0.50

Unit: mm



Weight: 0.02 g (typ.)

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