2N7002L, 2V7002L

Small Signal MOSFET

60 V, 115 mA, N-Channel SOT-23

Features

- 2V Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable (2V7002L)
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	60	Vdc
Drain-Gate Voltage (R_{GS} = 1.0 M Ω)	V _{DGR}	60	Vdc
Drain Current – Continuous $T_C = 25^{\circ}C$ (Note 1) $T_C = 100^{\circ}C$ (Note 1) – Pulsed (Note 2)	I _D I _D I _{DM}	±115 ±75 ±800	mAdc
Gate–Source Voltage – Continuous – Non–repetitive (t _p ≤ 50 μs)	V _{GS} V _{GSM}	±20 ±40	Vdc Vpk

THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Total Device Dissipation FR–5 Board (Note 3) T _A = 25°C Derate above 25°C Thermal Resistance, Junction–to–Ambient	P _D R _{θJA}	225 1.8 556	mW mW/°C °C/W
Total Device Dissipation (Note 4) Alumina Substrate, T _A = 25°C Derate above 25°C Thermal Resistance, Junction-to-Ambient	P _D R _{θJA}	300 2.4 417	mW mW/°C °C/W
Junction and Storage Temperature	T _J , T _{stg}	–55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. The Power Dissipation of the package may result in a lower continuous drain current.

2. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

3. FR-5 = 1.0 x 0.75 x 0.062 in.

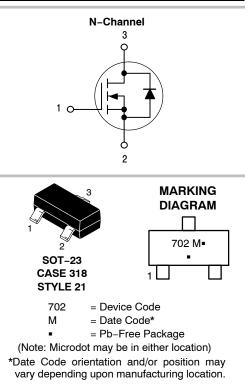
4. Alumina = 0.4 x 0.3 x 0.025 in 99.5% alumina.



ON Semiconductor®

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V _{(BR)DSS}	R _{DS(on)} MAX	I _D MAX
60 V	7.5 Ω @ 10 V, 500 mA	115 mA



ORDERING INFORMATION

Device	Package	Shipping [†]		
2N7002LT1G		3,000 Tape & Reel		
2N7002LT3G	SOT-23 (Pb-Free)	10,000 Tape & Reel		
2N7002LT7G		3,500 Tape & Reel		
2V7002LT1G		3,000 Tape & Reel		
2V7002LT3G	SOT-23 (Pb-Free)	10,000 Tape & Reel		
2N7002LT1H*		3,000 Tape & Reel		
2N7002LT7H*		3,500 Tape & Reel		

+For information on tape and reel specifications, including part orientation and tape sizes, please

refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*Not for new design.

2N7002L, 2V7002L

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

C	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Drain–Source Breakdown Volt (V_{GS} = 0, I _D = 10 μ Adc)	age	V _{(BR)DSS}	60	-	-	Vdc
Zero Gate Voltage Drain Curre $(V_{GS} = 0, V_{DS} = 60 \text{ Vdc})$	ent $T_J = 25^{\circ}C$ $T_J = 125^{\circ}C$	I _{DSS}	-		1.0 500	μAdc
Gate-Body Leakage Current, Forward (V _{GS} = 20 Vdc)		I _{GSSF}	-	-	100	nAdc
Gate-Body Leakage Current, (V _{GS} = -20 Vdc)	I _{GSSR}	-	-	-100	nAdc	
ON CHARACTERISTICS (Not	te 5)				•	•
Gate Threshold Voltage (V _{DS} = V _{GS} , I _D = 250 μAdd	;)	V _{GS(th)}	1.0	-	2.5	Vdc
$\begin{array}{l} On-State \ Drain \ Current \\ (V_{DS} \geq 2.0 \ V_{DS(on)}, \ V_{GS} = \end{array}$	I _{D(on)}	500	-	_	mA	
$\begin{array}{l} \text{Static Drain-Source On-State} \\ (\text{V}_{\text{GS}} = 10 \text{ Vdc}, \text{ I}_{\text{D}} = 500 \text{ m} \\ (\text{V}_{\text{GS}} = 5.0 \text{ Vdc}, \text{ I}_{\text{D}} = 50 \text{ m} \end{array}$	V _{DS(on)}			3.75 0.375	Vdc	
Static Drain–Source On–State (V _{GS} = 10 V, I _D = 500 mAd	$T_{C} = 25^{\circ}C$ $T_{C} = 125^{\circ}C$	r _{DS(on)}	- -		7.5 13.5	Ohms
(V _{GS} = 5.0 Vdc, I _D = 50 m/	Adc) $T_{C} = 25^{\circ}C$ $T_{C} = 125^{\circ}C$		-		7.5 13.5	
Forward Transconductance $(V_{DS} \ge 2.0 V_{DS(on)}, I_D = 20)$	0 mAdc)	9FS	80	-	-	mS
DYNAMIC CHARACTERISTIC	CS	11				
Input Capacitance (V _{DS} = 25 Vdc, V _{GS} = 0, f = 1.0 MHz)		C _{iss}	-	-	50	pF
Output Capacitance (V _{DS} = 25 Vdc, V _{GS} = 0, f = 1.0 MHz)		C _{oss}	-	-	25	pF
Reverse Transfer Capacitance $(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0, f$	C _{rss}	-	-	5.0	pF	
SWITCHING CHARACTERIS	TICS (Note 5)					
Turn-On Delay Time	$(V_{DD} = 25 \text{ Vdc}, I_D \cong 500 \text{ mAdc},$	t _{d(on)}	-	-	20	ns
Turn-Off Delay Time	$R_{G} = 25 \Omega, R_{L} = 50 \Omega, V_{gen} = 10 V$	t _{d(off)}	_	-	40	ns
BODY-DRAIN DIODE RATIN	GS					
Diode Forward On–Voltage ($I_S = 11.5 \text{ mAdc}, V_{GS} = 0 \text{ V}$	V _{SD}	-	-	-1.5	Vdc	
Source Current Continuous (Body Diode)		I _S	-	-	-115	mAdo
		1		1		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 5. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%.

 I_{SM}

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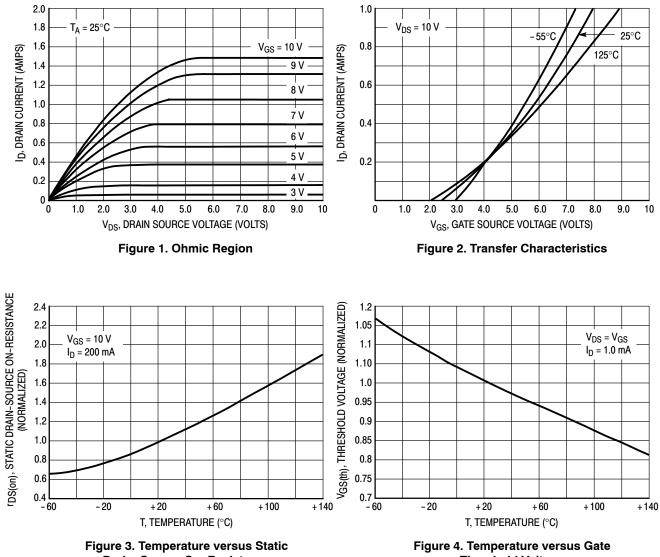
-800

mAdc

Source Current Pulsed

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TYPICAL ELECTRICAL CHARACTERISTICS

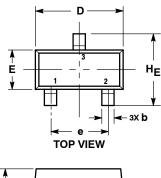


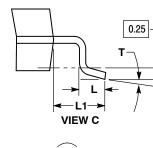
Drain-Source On-Resistance

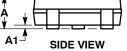
Threshold Voltage

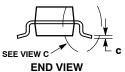
PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 **ISSUE AS**









NOTES: DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS.

MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF 3.

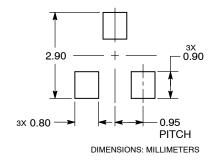
THE BASE MATERIAL. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. 4

Hornoolono, on date bonno.						
	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
С	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
Е	1.20	1.30	1.40	0.047	0.051	0.055
e	1.78	1.90	2.04	0.070	0.075	0.080
Г	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
Т	0°		10°	0°		10°

STYLE 21: PIN 1. GATE 2.

SOURCE 3. DRAIN

RECOMMENDED SOLDERING FOOTPRINT



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