



# PJM2302NSA

## N- Enhancement Mode Field Effect Transistor

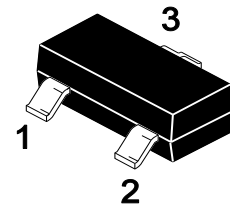
### Features

- Fast Switching
- Low Gate Charge and  $R_{DS(on)}$
- High power and current handling capability

### Applications

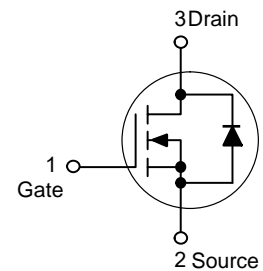
- Battery protection
- Load switch
- Power management

SOT-23



1. Gate 2. Source 3. Drain  
**Marking: M22**

### Schematic diagram



### Absolute Maximum Ratings

Ratings at  $T_C = 25^\circ\text{C}$  unless otherwise specified.

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 8$	V
Drain Current-Continuous	$I_D$	3.3	A
Drain Current-Pulsed <sup>Note 1</sup>	$I_{DM}$	16	A
Maximum Power Dissipation	$P_D$	0.9	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	$^\circ\text{C}$

### Thermal Characteristics

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient <sup>Note 2</sup>	$R_{\theta JA}$	139	$^\circ\text{C}/\text{W}$



**Electrical Characteristics**

T<sub>A</sub>=25°C unless otherwise noted

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	20	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V	-	-	±100	nA
Gate Threshold Voltage <sup>Note3</sup>	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.5	0.75	1.2	V
Drain-Source On-State Resistance <sup>Note3</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> =2.5V, I <sub>D</sub> =2.8A	-	35	60	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A	-	29	45	mΩ
Forward Transconductance <sup>Note3</sup>	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =3A	-	8	-	S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, f=1.0MHz	-	300	-	pF
Output Capacitance	C <sub>oss</sub>		-	120	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	80	-	pF
<b>Switching Characteristics</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =10V, I <sub>D</sub> =3A V <sub>GS</sub> =4.5V, R <sub>GEN</sub> =6Ω	-	10	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	50	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	17	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	10	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =3A, V <sub>GS</sub> =4.5V	-	4	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	0.7	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	1.5	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <sup>Note 3</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =3.3A	-	0.75	1.2	V
Diode Forward Current <sup>Note 2</sup>	I <sub>S</sub>		-	-	3.3	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.



Typical Characteristics Curves

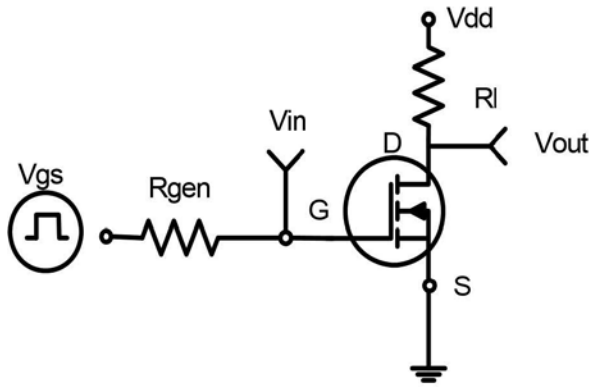


Figure 1: Switching Test Circuit

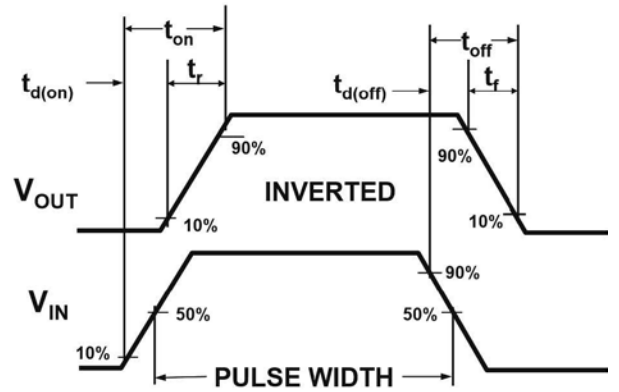


Figure 2: Switching Waveforms

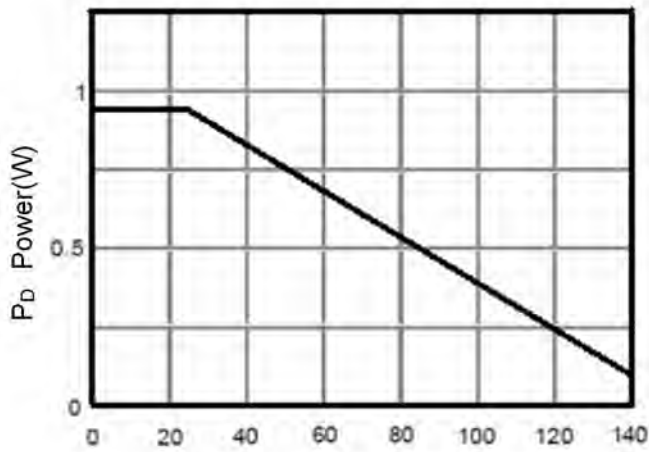


Figure 3 Power Dissipation

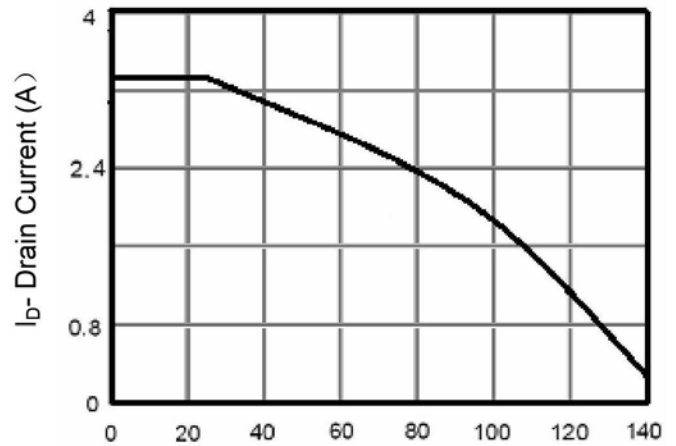


Figure 4 Drain Current

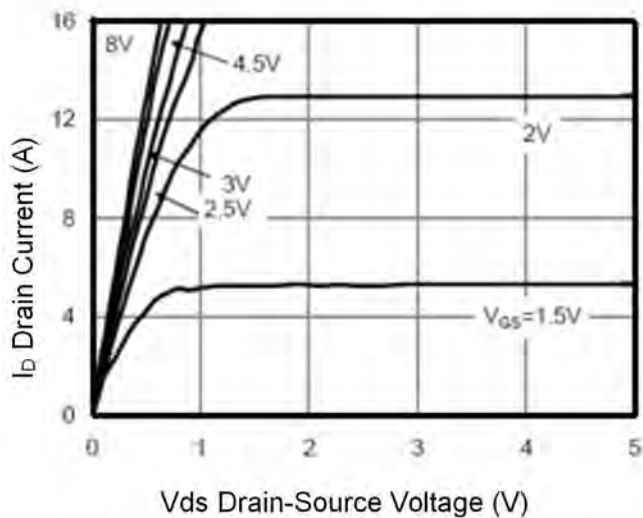


Figure 5 Output Characteristics

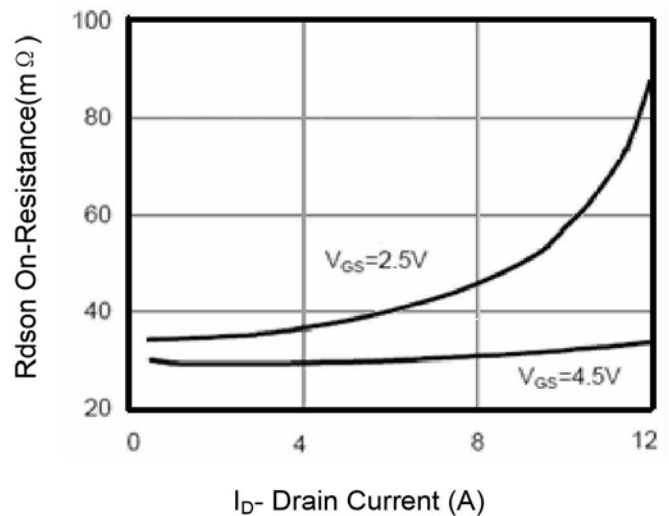


Figure 6 Drain-Source On-Resistance

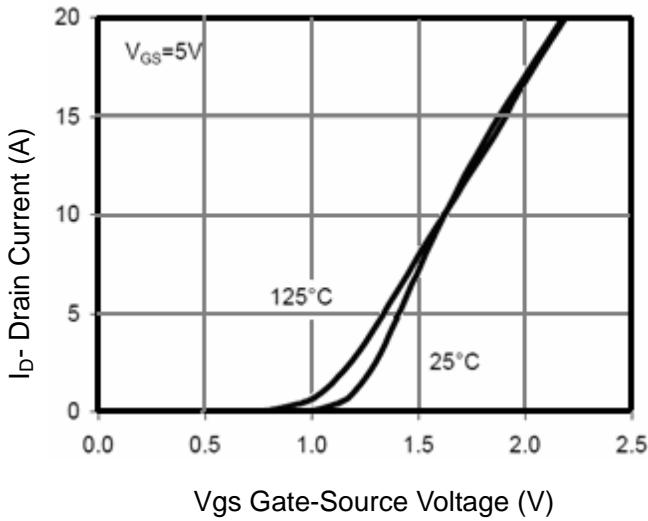


Figure 7 Transfer Characteristics

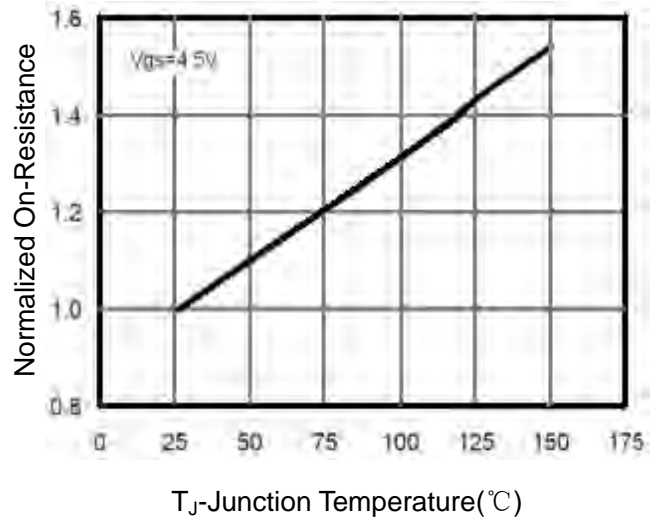


Figure 8 Drain-Source On-Resistance

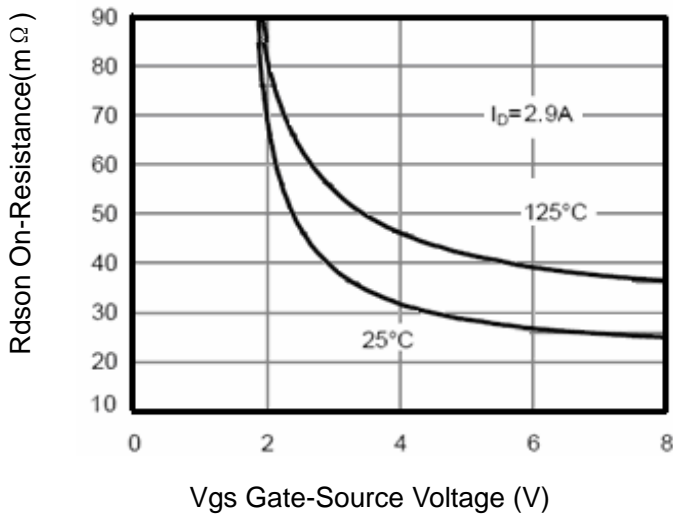


Figure 9 Rdson vs Vgs

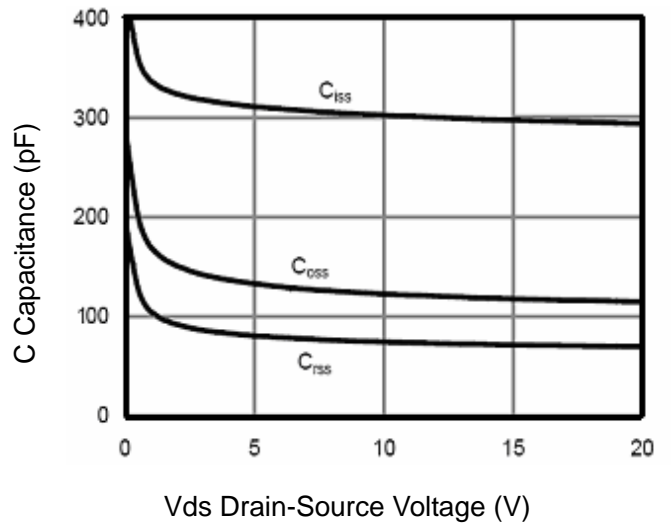


Figure 10 Capacitance vs Vds

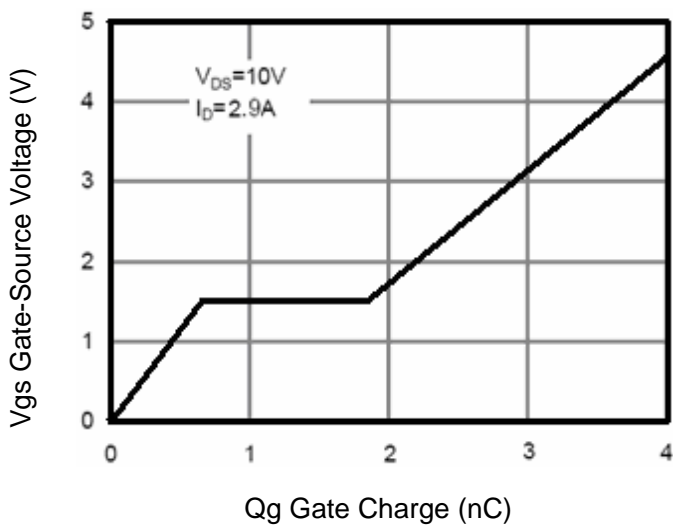


Figure 11 Gate Charge

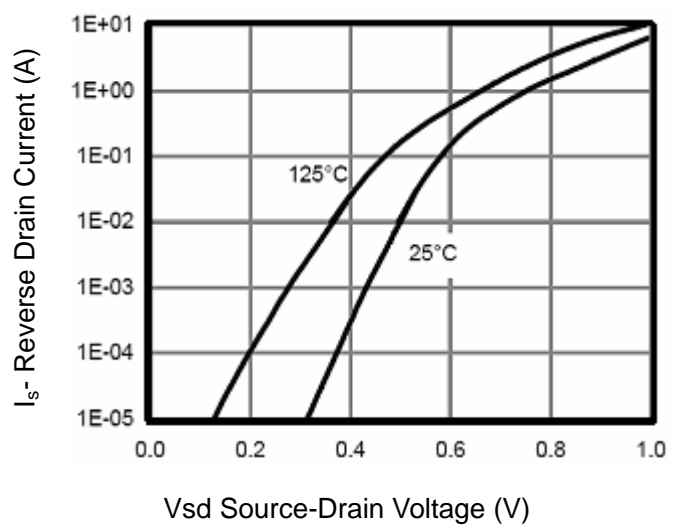
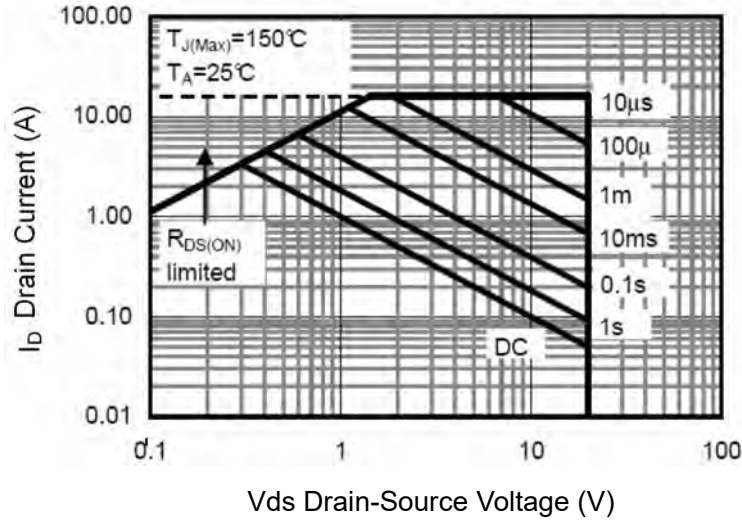
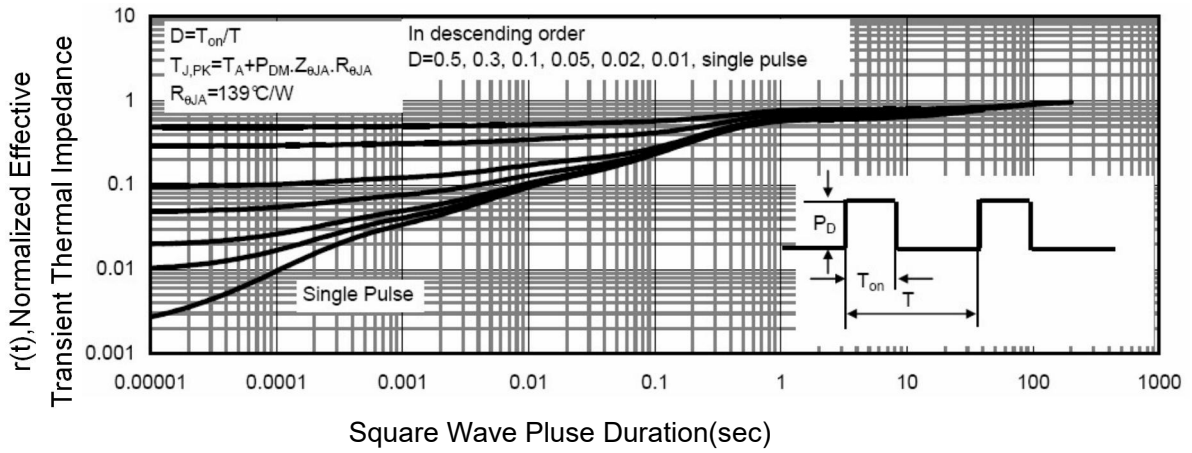


Figure 12 Source- Drain Diode Forward



**Figure 13 Safe Operation Area**



**Figure 14 Normalized Maximum Transient Thermal Impedance**

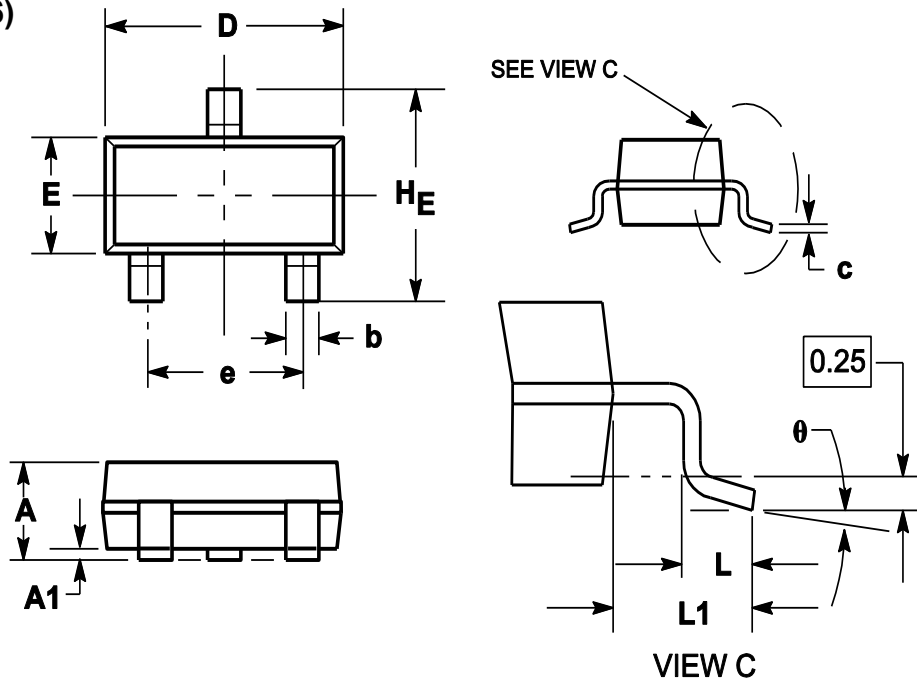


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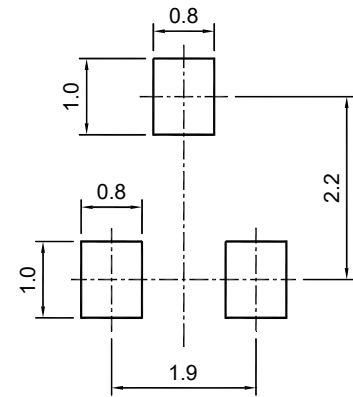
## N- Enhancement Mode Field Effect Transistor

### Package Outline

#### SOT-23 (TO-236)



Symbol	Dimensions in millimeter		
	Min.	Typ.	Max.
A	0.900	1.025	1.150
A1	0.000	0.050	0.100
b	0.300	0.400	0.500
c	0.080	0.115	0.150
D	2.800	2.900	3.000
E	1.200	1.300	1.400
HE	2.250	2.400	2.550
e	1.800	1.900	2.000
L1	0.550REF		
L	0.300		0.500
$\theta$	0°		8°

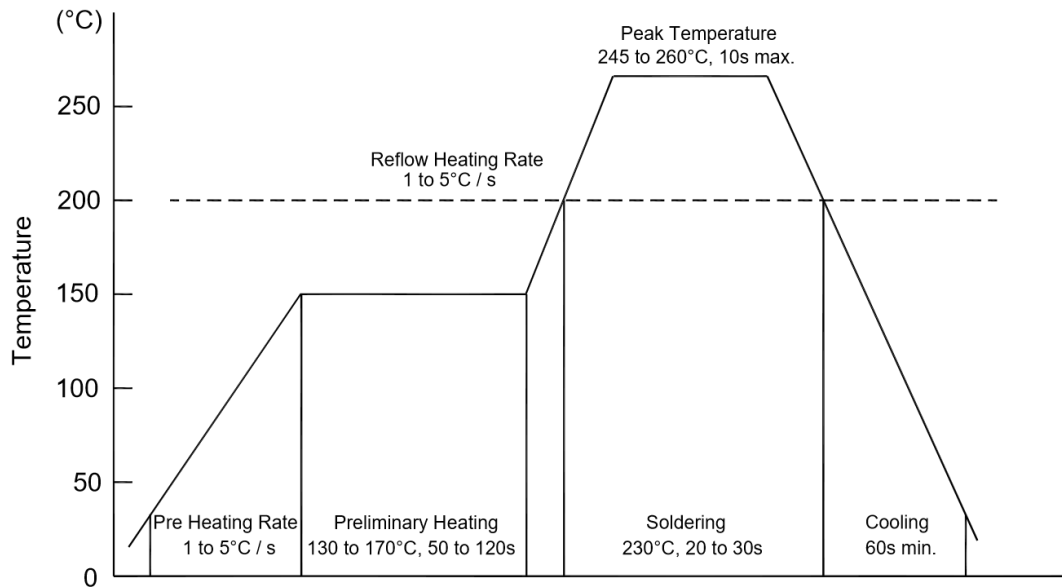


SOT-23 (TO-236)

**Recommended soldering pad**

### Ordering Information

Device	Package	Shipping
PJM2302NSA	SOT-23	3000/Reel&Tape(7inch)

**Conditions of Soldering and Storage****◆ Recommended condition of reflow soldering**

Recommended peak temperature is over 245 °C. If peak temperature is below 245 °C, you may adjust the following parameters:

- Time length of peak temperature (longer)
- Time length of soldering (longer)
- Thickness of solder paste (thicker)

**◆ Conditions of hand soldering**

- Temperature: 370 °C
- Time: 3s max.
- Times: one time

**◆ Storage conditions**

- **Temperature**  
5 to 40 °C
- **Humidity**  
30 to 80% RH
- **Recommended period**  
One year after manufacturing

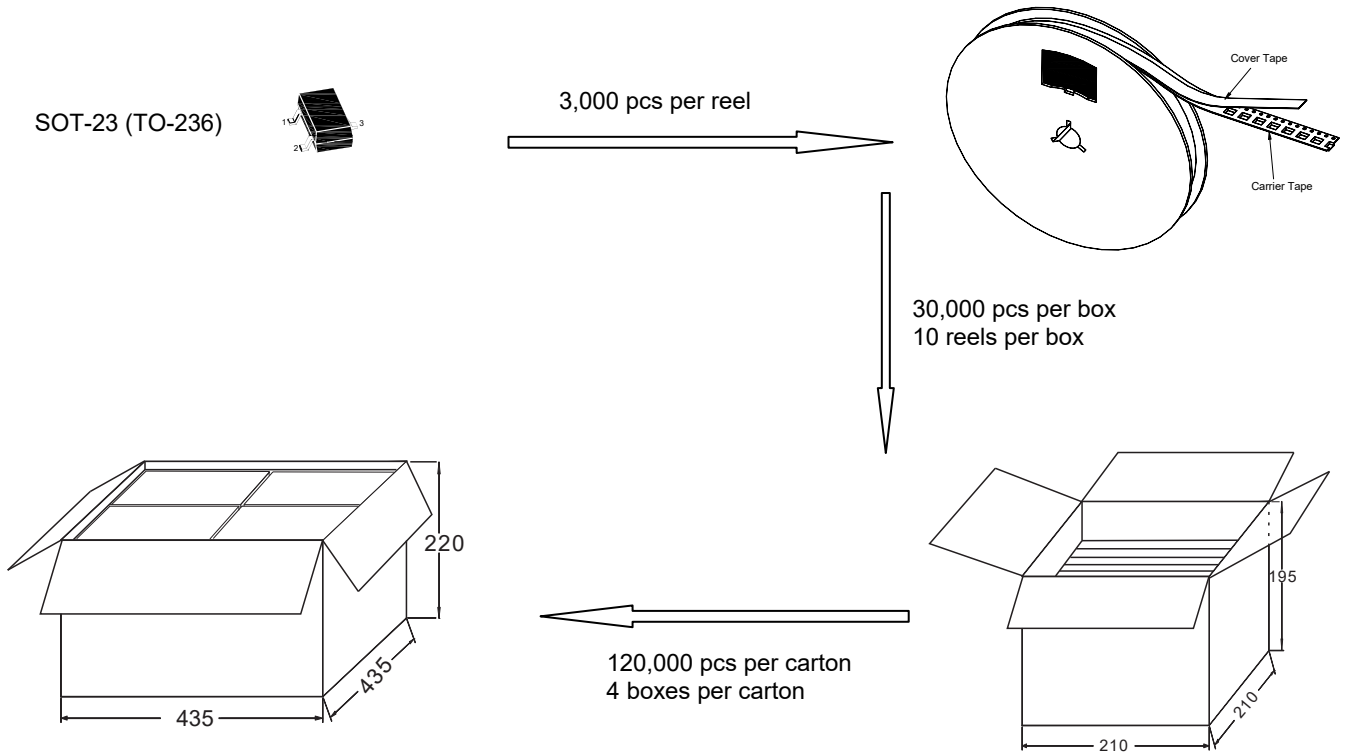


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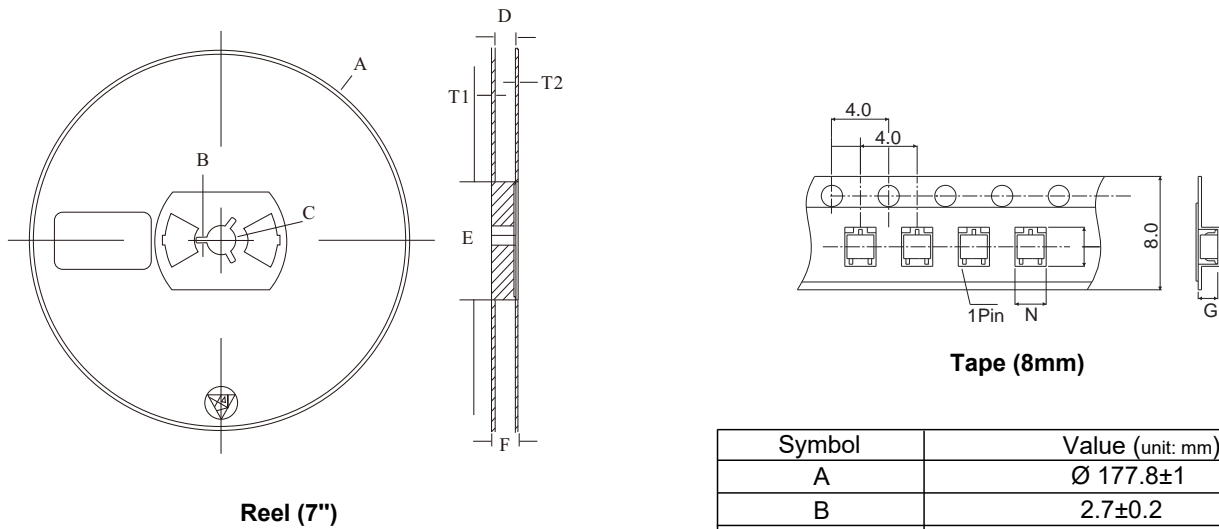
## N- Enhancement Mode Field Effect Transistor

### Package Specifications

#### ◆ The method of packaging



#### ◆ Embossed tape and reel data



Symbol	Value (unit: mm)
A	∅ 177.8±1
B	2.7±0.2
C	∅ 13.5±0.2
E	∅ 54.5±0.2
F	12.3±0.3
D	9.6+2/-0.3
T1	1.0±0.2
T2	1.2±0.2
N	3.15±0.1
G	1.25±0.1