Product data sheet

1. General description

NPN switching transistor in an ultra small DFN1010D-3 (SOT1215) leadless Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

PNP complement: PMBT2907AQA

2. Features and benefits

- High current (max. 600 mA)
- Low voltage (max. 40V)
- · Leadless ultra small SMD plastic package
- Low package height of 0.37 mm
- Suitable for Automatic Optical Inspection (AOI) of solder joint

3. Applications

- Switching and linear applications
- Mobile applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	40	V
I _C	collector current			-	-	600	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	800	mA
h _{FE}	DC current gain	V _{CE} = 10 V; I _C = 150 mA	[1]	100	-	300	
		V _{CE} = 10 V; I _C = 500 mA	[1]	40	-	-	

[1] Pulsed test: $t_p \le 300 \,\mu s$; $\delta \le 0.02$



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		С
2	Е	emitter		В
3	С	collector	4 3	в
4	С	collector	2	E sym123
			Transparent top view	
			DFN1010D-3 (SOT1215)	

6. Ordering information

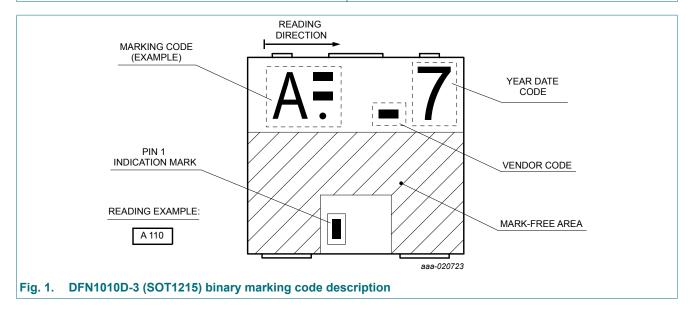
Table 3. Ordering information

Type number Package							
	Name	Description	Version				
PMBT2222AQA		plastic, leadless thermal enhanced ultra thin small outline package; 3 terminals; 0.75 mm pitch; 1.1 mm x 1 mm x 0.37 mm body	SOT1215				

7. Marking

Table 4. Marking codes

Type number	Marking code
PMBT2222AQA	X 100



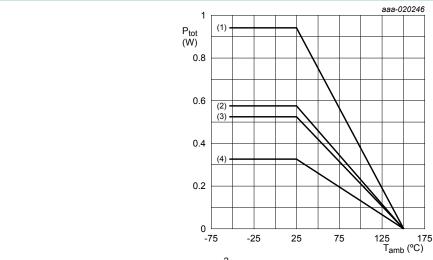
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	75	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	600	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	800	mA
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	325	mW
			[2]	-	575	mW
			[3]	-	525	mW
			[4]	-	940	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated; mounting pad for collector 1 cm².
- [3] Device mounted on an FR4 Printed-Circuit Board (PCB), 4-layer copper, tin-plated and standard footprint.
- [4] Device mounted on an FR4 Printed-Circuit Board (PCB), 4-layer copper, tin-plated; mounting pad for collector 1 cm².



- (1) FR4 PCB, 4-layer copper, 1 cm²
- (2) FR4 PCB, single sided copper, 1 cm²
- (3) FR4 PCB, 4-layer copper, standard footprint
- (4) FR4 PCB, single sided copper, standard footprint

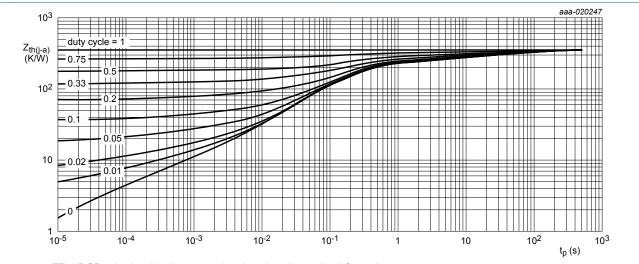
Fig. 2. Power derating curve DFN1010D-3 (SOT1215)

9. Thermal characteristics

Table 6. Thermal characteristics

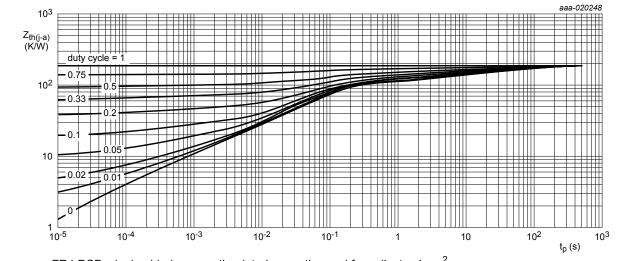
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
uily u)		nermal resistance from in free air unction to ambient	[1]	-	-	385	K/W
	junction to ambient		[2]	-	-	218	K/W
			[3]	-	-	239	K/W
		[4]	-	-	133	K/W	

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated; mounting pad for collector 1 cm².
- [3] Device mounted on an FR4 Printed-Circuit Board (PCB), 4-layer copper, tin-plated and standard footprint.
- [4] Device mounted on an FR4 Printed-Circuit Board (PCB), 4-layer copper, tin-plated; mounting pad for collector 1 cm².



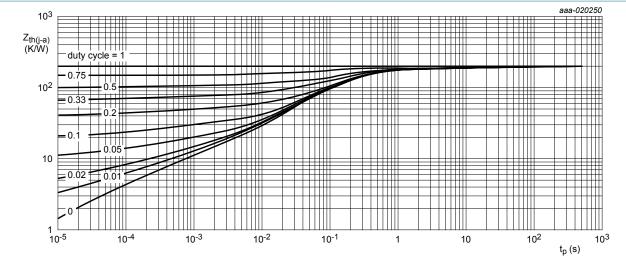
FR4 PCB, single-sided copper, tin-plated and standard footprint

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



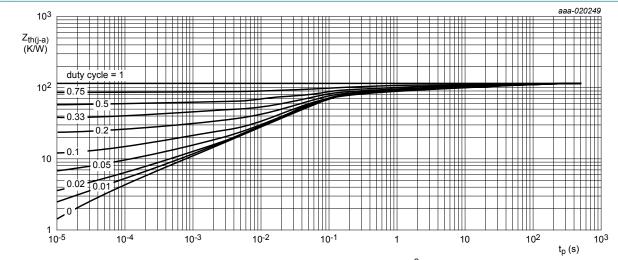
FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 1 cm²

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, 4-layer copper, tin-plated and standard footprint

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, 4-layer copper, tin-plated; mounting pad for collector 1 cm²

Fig. 6. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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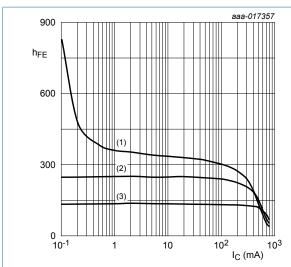
10. Characteristics

Table 7. Characteristics

 T_{amb} = 25 °C unless otherwise specified

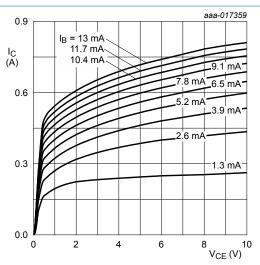
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A		75	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}$; $I_B = 0 \text{ A}$		40	-	-	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	I _C = 0 A; I _E = 100 μA		6	-	-	V
I _{CBO}	collector-base cut-off	V _{CB} = 60 V; I _E = 0 A		-	-	10	nA
	current	V _{CB} = 60 V; I _E = 0 A; T _j = 125 °C		-	-	10	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A		-	-	10	nA
h _{FE} [DC current gain	V _{CE} = 10 V; I _C = 100 μA		35	-	-	
		V _{CE} = 10 V; I _C = 1 mA		50	-	-	
		V _{CE} = 10 V; I _C = 10 mA		75	-	-	
		V _{CE} = 10 V; I _C = 10 mA; T _{amb} = -55 °C		35	-	-	
		V _{CE} = 10 V; I _C = 150 mA	[1]	100	-	300	
		V _{CE} = 1 V; I _C = 150 mA	[1]	50	-	-	
		V _{CE} = 10 V; I _C = 500 mA	[1]	40	-	-	
V _{CEsat}	collector-emitter	I _C = 150 mA; I _B = 15 mA	[1]	-	-	300	mV
	saturation voltage	I _C = 500 mA; I _B = 50 mA	[1]	-	-	1	V
V _{BEsat}	base-emitter saturation	I _C = 150 mA; I _B = 15 mA	[1]	0.6	-	1.2	V
	voltage	I _C = 500 mA; I _B = 50 mA	[1]	-	-	2	V
t _d	delay time	I _C = 150 mA; I _{Bon} = 15 mA;		-	-	15	ns
t _r	rise time	I _{Boff} = -15 mA		-	-	20	ns
t _{on}	turn-on time			-	-	35	ns
t _s	storage time			-	-	200	ns
t _f	fall time			-	-	60	ns
t _{off}	turn-off time			-	-	260	ns
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}$		-	-	8	pF
C _e	emitter capacitance	V_{EB} = 500 mV; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz		-	-	25	pF
f _T	transition frequency	V _{CE} = 20 V; I _C = 20 mA; f = 100 MHz	[1]	-	340	-	MHz

^[1] Pulsed test: $t_p \le 300 \ \mu s; \ \delta \le 0.02$



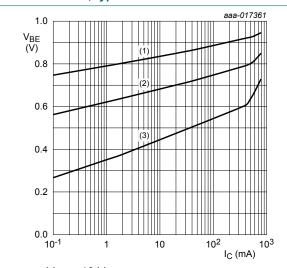
V_{CE} = 10 V (1) T_{amb} = 100 °C (2) T_{amb} = 25 °C (3) T_{amb} = -55 °C





 T_{amb} = 25 °C

Fig. 8. Collector current as a function of collectoremitter voltage; typical values

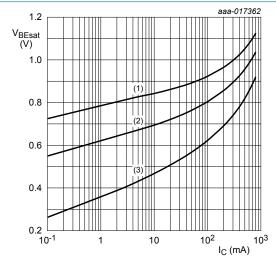


 V_{CE} = 10 V

(1) $T_{amb} = -55 \,^{\circ}C$ (2) $T_{amb} = 25 \,^{\circ}C$

(3) $T_{amb} = 150 \, ^{\circ}C$

Fig. 9. Base-emitter voltage as a function of collector current; typical values



 $I_C/I_B = 10$

(1) $T_{amb} = -55$ °C

(2) $T_{amb} = 25 \, ^{\circ}C$

(3) $T_{amb} = 150 \, ^{\circ}C$

Fig. 10. Base-emitter saturation voltage as a function of collector current; typical values

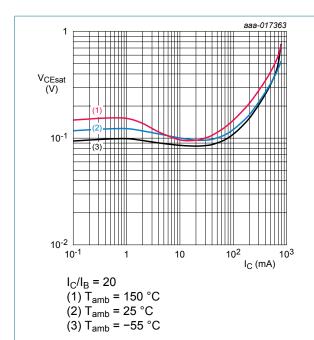


Fig. 11. Collector-emitter saturation voltage as a function of collector current; typical values

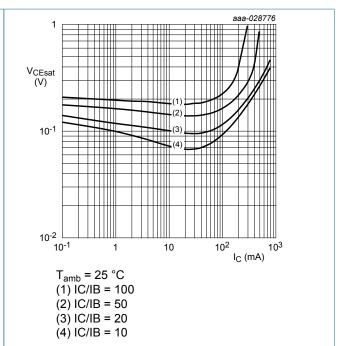
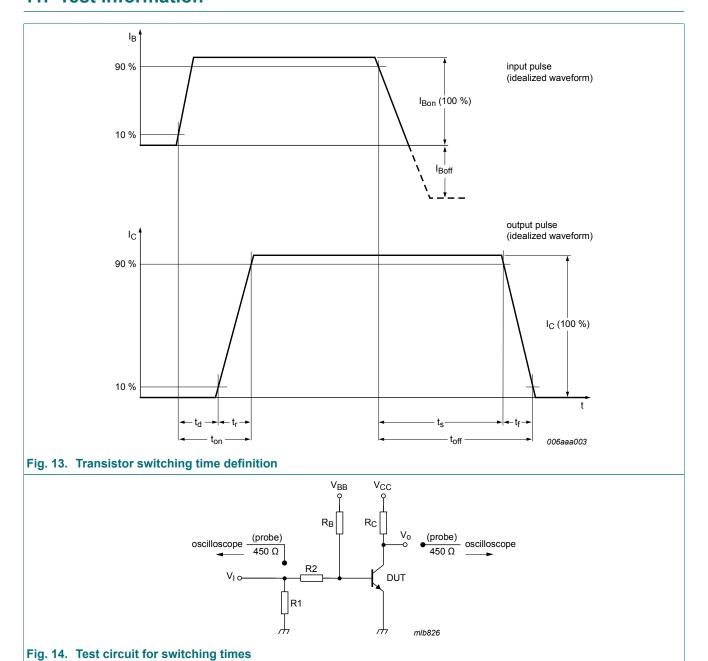


Fig. 12. Collector-emitter saturation voltage as a function of collector current; typical values

11. Test information



12. Package outline

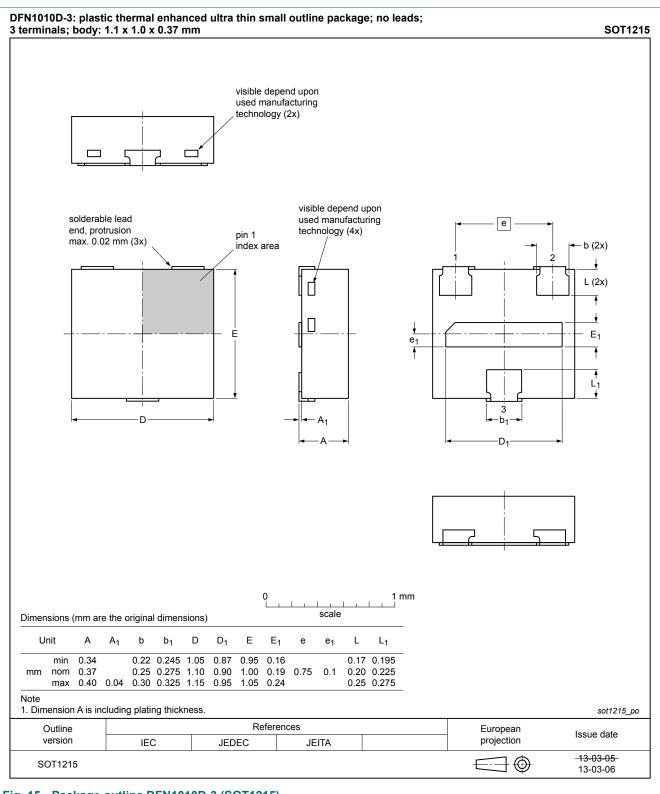
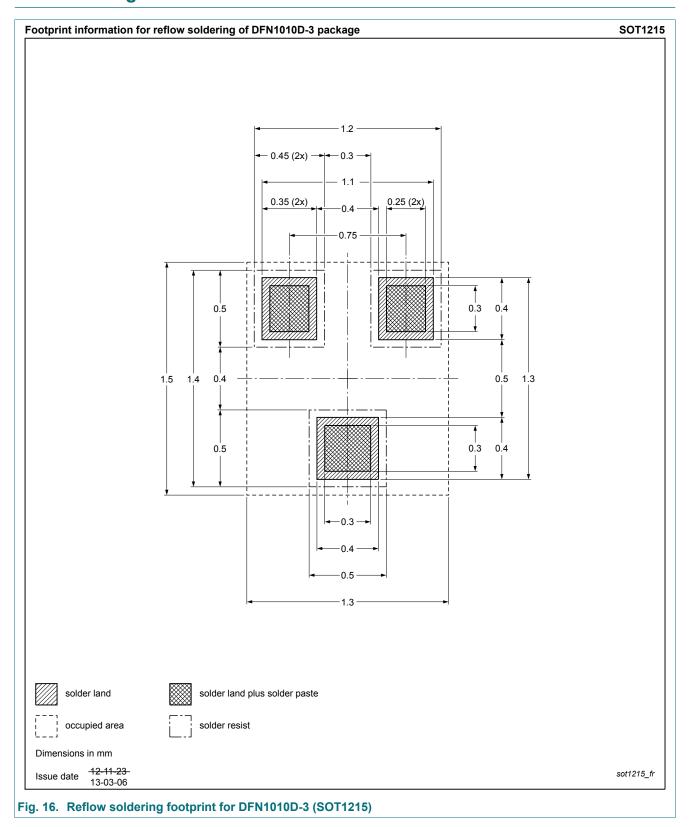


Fig. 15. Package outline DFN1010D-3 (SOT1215)

13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMBT2222AQA v.1	20180921	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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