

2N7002CK 60 V, 0.3 A N-channel Trench MOSFET Rev. 01 — 11 September 2009

Product data sheet

1. Product profile

1.1 General description

ESD protected N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

1.2 Features

- Logic-level compatible
- Very fast switching
- Trench MOSFET technology
- ESD protection up to 3 kV

1.3 Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{DS}	drain-source voltage		-	-	60	V
I _D	drain current		-	-	300	mA
I _{DM}	peak drain current	single pulse; t _p ≤ 10 μs	-	-	1.2	A
R_{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 500 mA	-	1.1	1.6	Ω



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2. Pinning information

Table 2.	Pinning			
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		2
2	S	source		D
3	D	drain		
				017aaa000

3. Ordering information

Table 3. Ordering information Type number Package

	U			
	Name	Description	Version	
2N7002CK	TO-236AB	plastic surface-mounted package; 3 leads	SOT23	

4. Marking

Table 4. N	larking codes	
Type numbe	r	Marking code ^[1]
2N7002CK		LP*

[1] * = -: made in Hong Kong

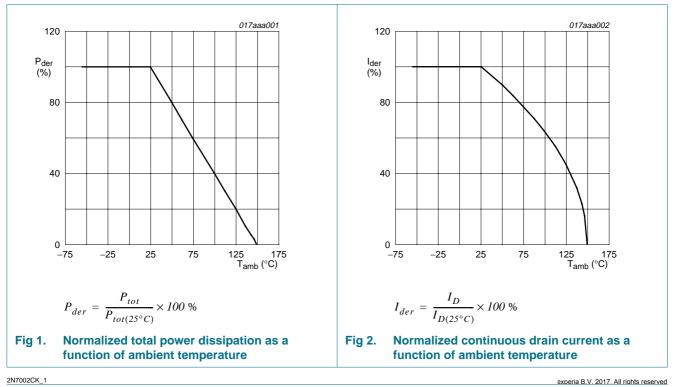
- * = p: made in Hong Kong
- * = t: made in Malaysia
- * = W: made in China

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5. Limiting values

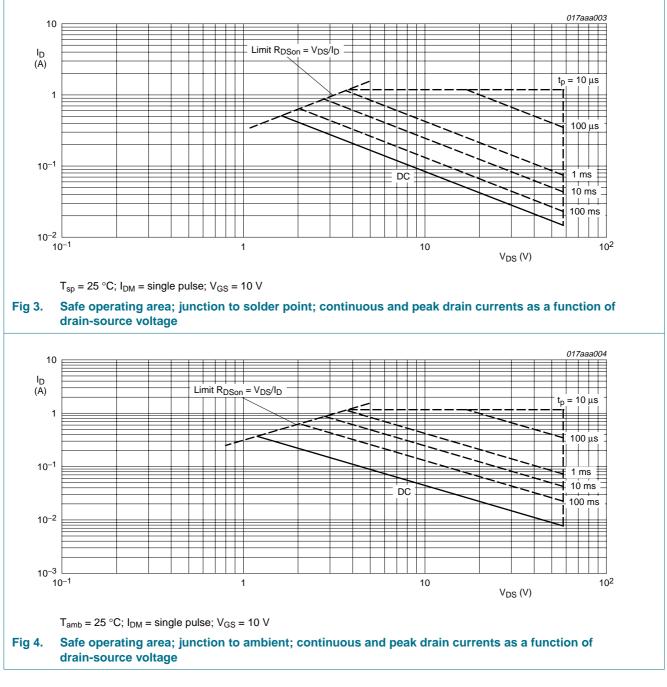
Table 5. In accorda	Limiting values ance with the Absolute Max	ximum Rating System (IEC 6	60134).		
Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	drain-source voltage	25 °C \leq T _j \leq 150 °C	-	60	V
V _{GS}	gate-source voltage		-	±20	V
I _D	drain current	V _{GS} = 10 V			
		T _{amb} = 25 °C	-	300	mA
		T _{amb} = 100 °C	-	190	mA
I _{DM}	peak drain current	T_{amb} = 25 °C; $t_p \le 10 \ \mu s$	-	1.2	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	<u>[1]</u> _	350	mW
Tj	junction temperature			150	°C
T _{amb}	ambient temperature		-55	+150	°C
T _{stg}	storage temperature		-65	+150	°C
Source-d	rain diode				
I _S	source current	T _{amb} = 25 °C	-	200	mA
I _{SM}	peak source current	T_{amb} = 25 °C; $t_p \le 10 \ \mu s$	-	1.2	А
ElectroSt	atic Discharge (ESD)				
V _{ESD}	electrostatic discharge voltage	all pins; human body model; C = 100 pF; R = 1.5 k Ω	-	3	kV

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².



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6. Thermal characteristics

Table 6.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	<u>[1]</u> _	350	500	K/W

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Table 6.	Thermal characteristics continued					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point		-	-	150	K/W

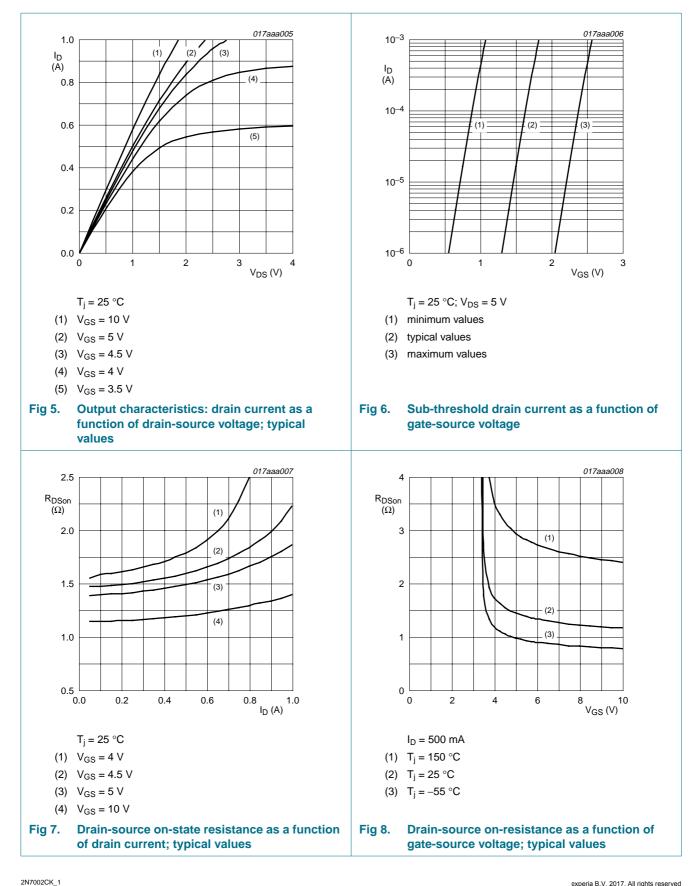
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics					
V _{(BR)DSS}	drain-source breakdown	$I_D = 10 \ \mu A; \ V_{GS} = 0 \ V$				
	voltage	T _j = 25 °C	60	-	-	V
		T _j = −55 °C	55	-	-	V
V _{GS(th)}	gate-source threshold voltage	$\begin{split} I_D &= 250 \; \mu\text{A}; \; V_{DS} = V_{GS}; \\ T_j &= 25 \; ^\circ\text{C} \end{split}$	1	1.75	2.5	V
I _{DSS}	drain leakage current	$V_{DS} = 60 \text{ V}; V_{GS} = 0 \text{ V}$				
		T _j = 25 °C	-	-	100	nA
		T _j = 150 °C	-	-	1	μΑ
I _{GSS} gate leak	gate leakage current	V_{GS} = ±20 V; V_{DS} = 0 V	-	-	5	μΑ
		V_{GS} = ±10 V; V_{DS} = 0 V	-	50	450	nA
		V_{GS} = ±5 V; V_{DS} = 0 V	-	-	100	nA
R _{DSon} drain-sourc resistance	drain-source on-state resistance	V _{GS} = 4.5 V; I _D = 200 mA				
		T _j = 25 °C	-	1.3	3	Ω
		T _j = 150 °C	-	2.8	4.4	Ω
		V_{GS} = 10 V; I _D = 500 mA	-	1.1	1.6	Ω
Dynamic o	characteristics					
Q _{G(tot)}	total gate charge	I _D = 200 mA;	-	1.09	1.3	nC
Q _{GS}	gate-source charge	V _{DS} = 10 V; V _{GS} = 4.5 V	-	0.22	-	nC
Q _{GD}	gate-drain charge	VGS - 4.0 V	-	0.23	-	nC
C _{iss}	input capacitance	$V_{GS} = 0 V; V_{DS} = 25 V;$	-	47.2	55	pF
C _{oss}	output capacitance	f = 1 MHz	-	11	20	pF
C _{rss}	reverse transfer capacitance		-	5	7.5	pF
t _{d(on)}	turn-on delay time	V _{DS} = 15 V;	-	8	15	ns
t _r	rise time	$R_L = 15 \Omega;$ V _{GS} = 10 V;	-	8	15	ns
t _{d(off)}	turn-off delay time	$R_{G} = 6 \Omega$	-	38	50	ns
t _f	fall time	-	-	22	35	ns
Source-dr	ain diode					
V _{SD}	source-drain voltage	I _S = 200 mA; V _{GS} = 0 V	0.47	0.79	1.1	V

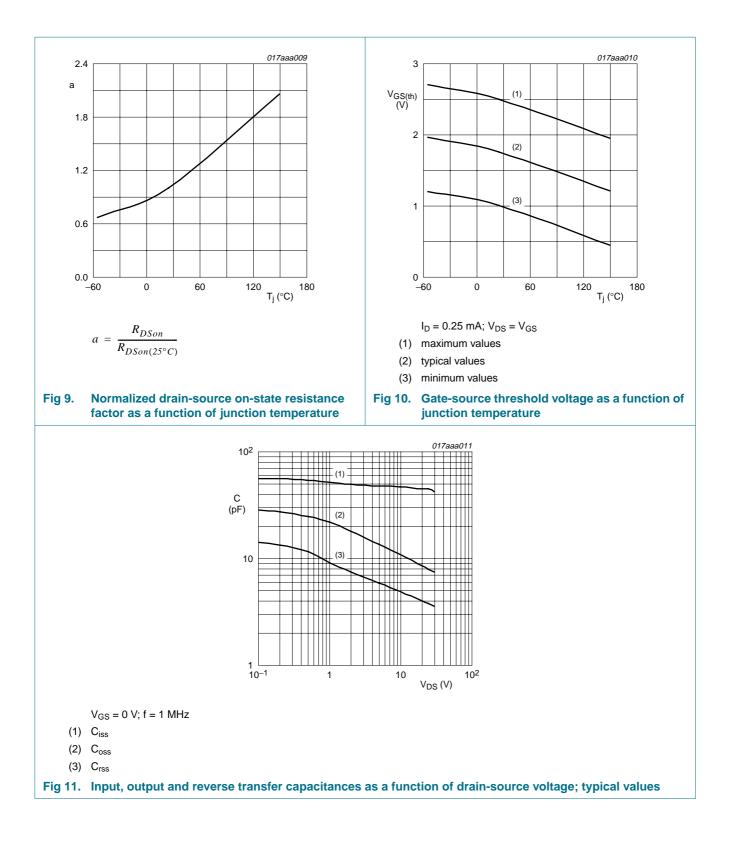
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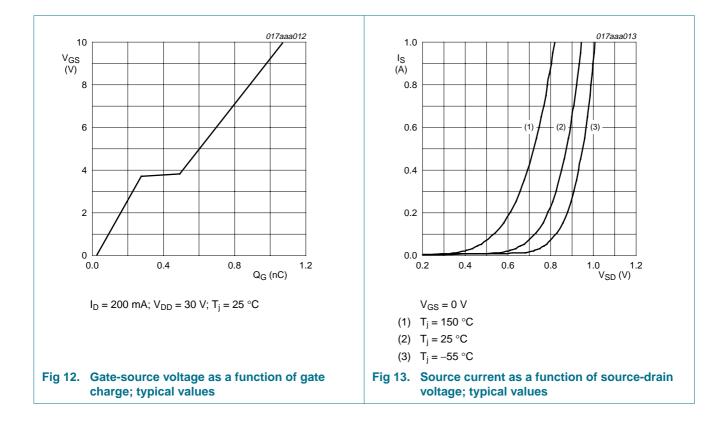
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8. Package outline

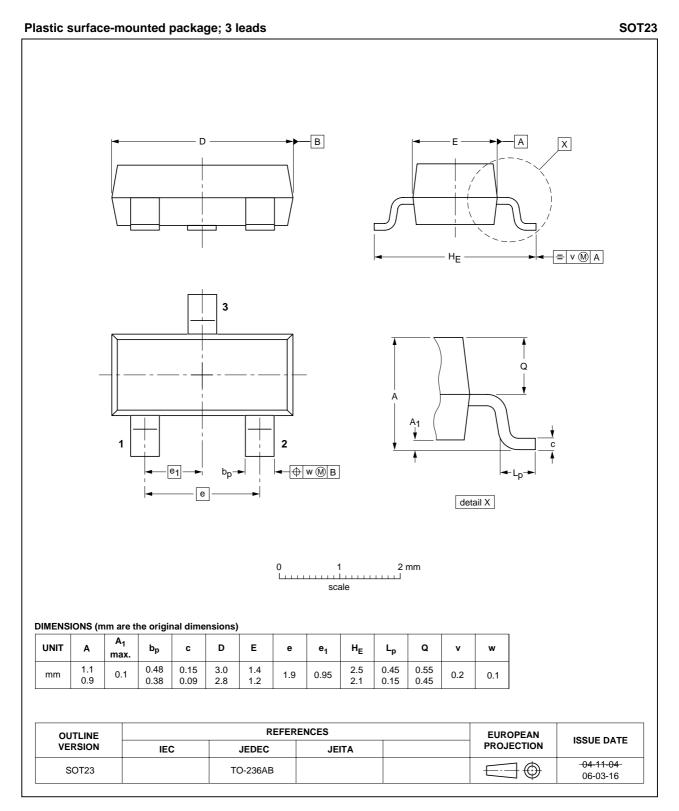
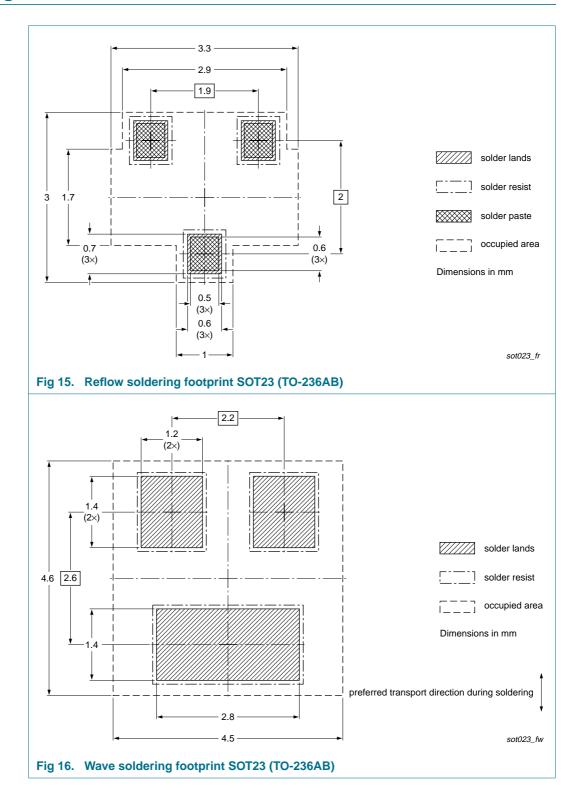


Fig 14. Package outline SOT23 (TO-236AB)

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9. Soldering



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10. Revision history

Table 8.	Revision history				
Document	: ID	Release date	Data sheet status	Change notice	Supersedes
2N7002CK	<u>_</u> 1	20090911	Product data sheet	-	-

11. Legal information

11.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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