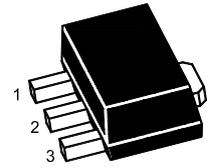


# PJM3400NSQ

## N- Enhancement Mode Field Effect Transistor

SOT-89



1. Gate 2.Drain 3.Source

Marking: U0

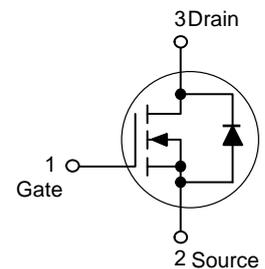
### Features

- $V_{DS} = 30V, I_D = 6A$   
 $R_{DS(ON)} < 45m\Omega @ V_{GS}=2.5V$   
 $R_{DS(ON)} < 31m\Omega @ V_{GS}=4.5V$   
 $R_{DS(ON)} < 27m\Omega @ V_{GS}=10V$
- High power and current handling capability

### Applications

- Load switch and in PWM applications
- Power management

### Schematic diagram



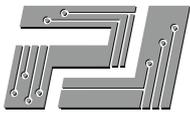
### Absolute Maximum Ratings

Ratings at 25°C ambient temperature unless otherwise specified.

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Drain Current-Continuous	$I_D$	6	A
Drain Current-Pulsed <sup>Note1</sup>	$I_{DM}$	30	A
Maximum Power Dissipation	$P_D$	1.4	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	150, -55 To 150	°C

### Thermal Characteristics

Thermal Resistance, Junction-to-Ambient <sup>Note2</sup>	$R_{\theta JA}$	89	°C/W
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**Electrical Characteristics**

(T<sub>A</sub>=25°C unless otherwise noted)

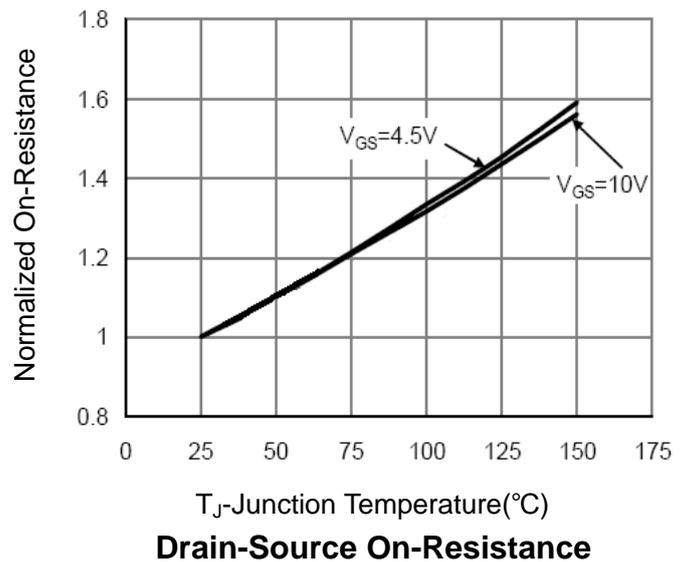
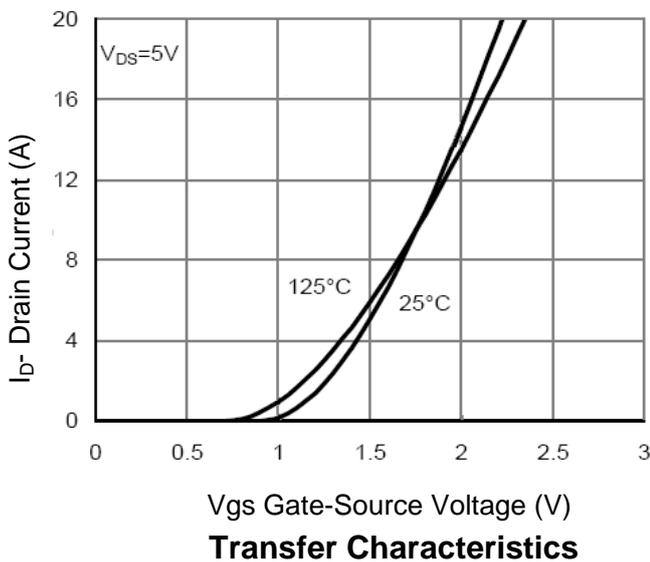
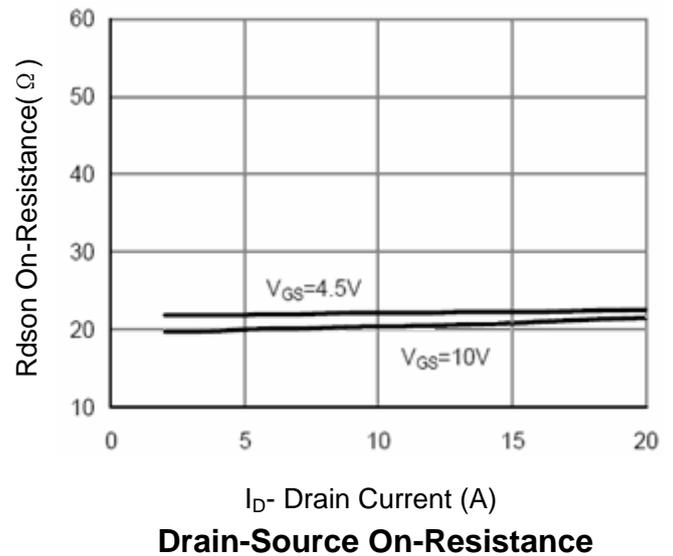
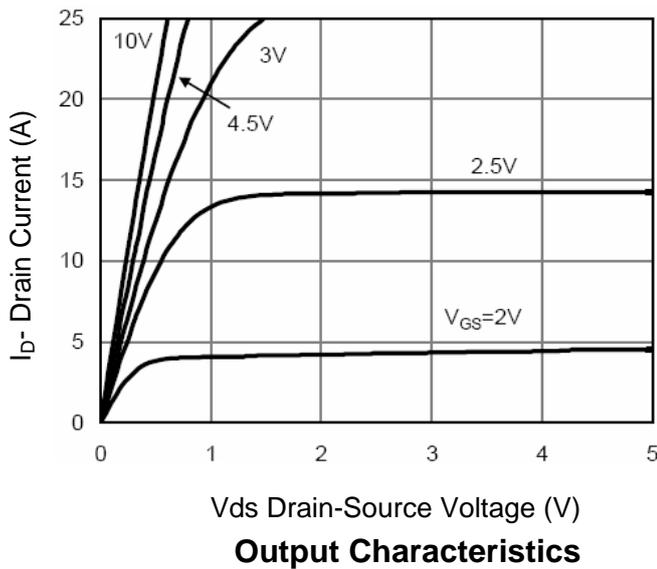
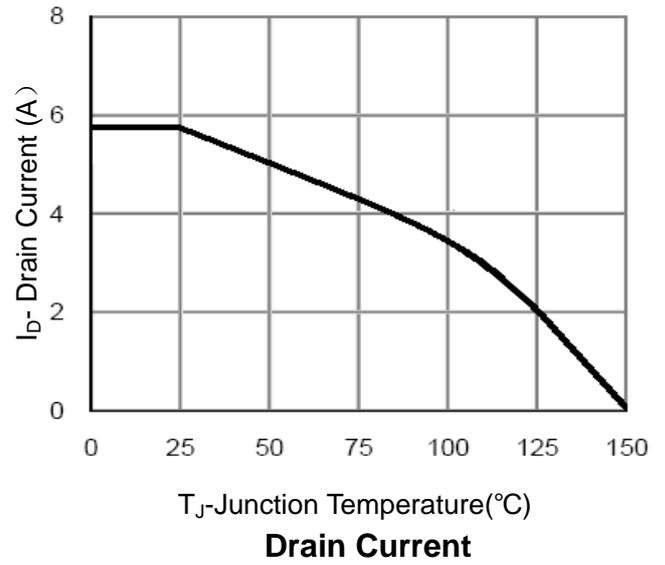
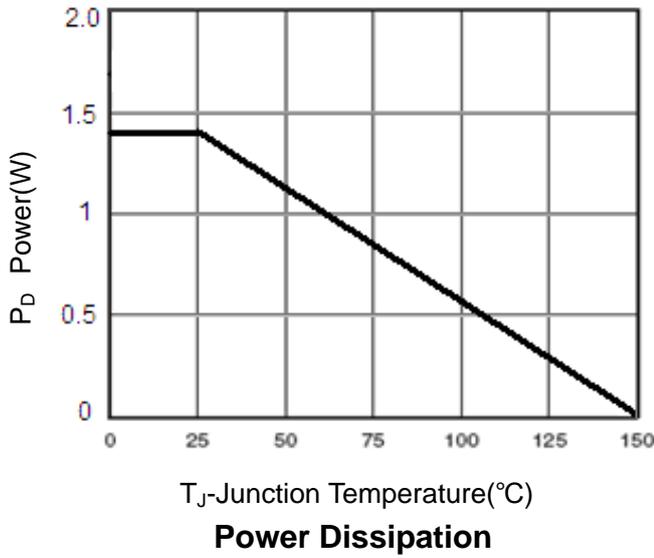
Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	30	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0V	-	-	±100	nA
Gate Threshold Voltage <sup>Note3</sup>	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.7	0.9	1.4	V
Drain-Source On-State Resistance <sup>Note3</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> =2.5V, I <sub>D</sub> =4A	-	-	45	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A	-	-	31	mΩ
		V <sub>GS</sub> =10V, I <sub>D</sub> =5.8A	-	-	27	mΩ
Forward Transconductance <sup>Note3</sup>	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =5A	10	-	-	S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1.0MHz	-	825	-	pF
Output Capacitance	C <sub>oss</sub>		-	100	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	78	-	pF
<b>Switching Characteristics</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =15V, R <sub>L</sub> =2.7Ω V <sub>GS</sub> =10V, R <sub>GEN</sub> =3Ω	-	3.3	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	4.8	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	26	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	4	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =5.8A, V <sub>GS</sub> =4.5V	-	10	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	1.6	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	3.1	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <sup>Note3</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =5.8A	-	-	1.2	V
Diode Forward Current <sup>Note2</sup>	I <sub>S</sub>		-	-	5.8	A

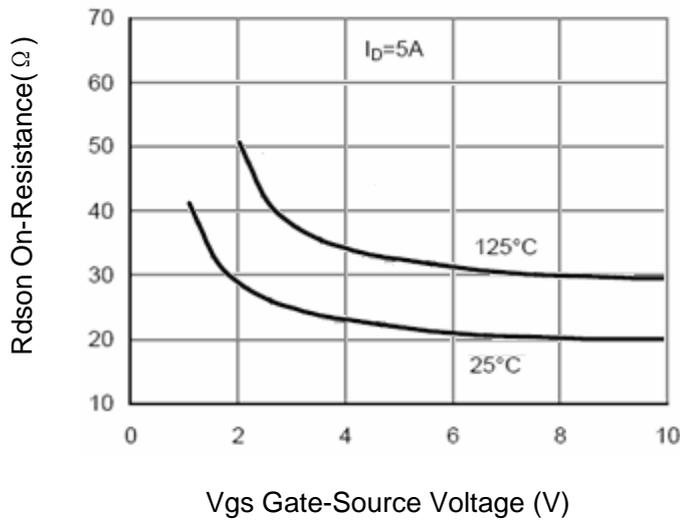
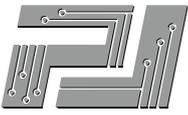
Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.

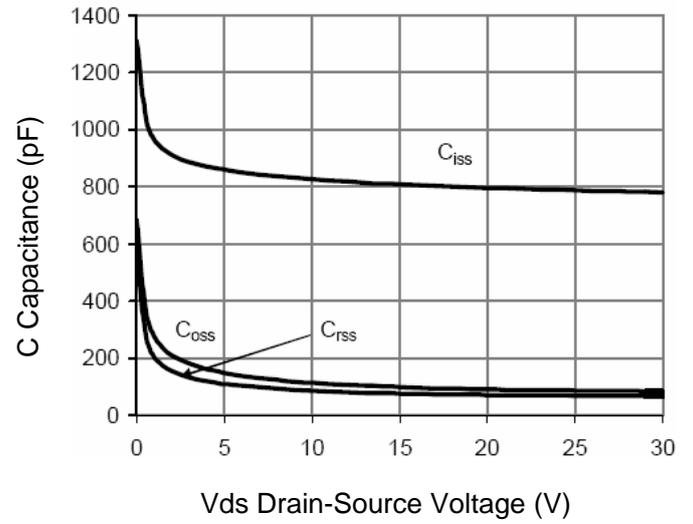


### Typical Characteristics Curves

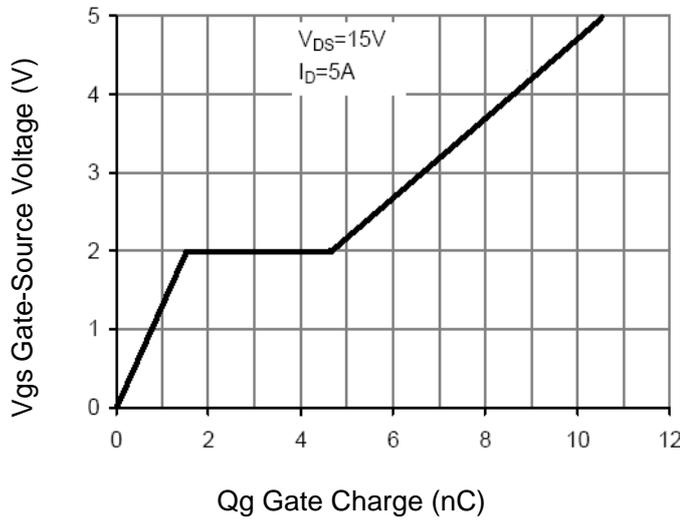




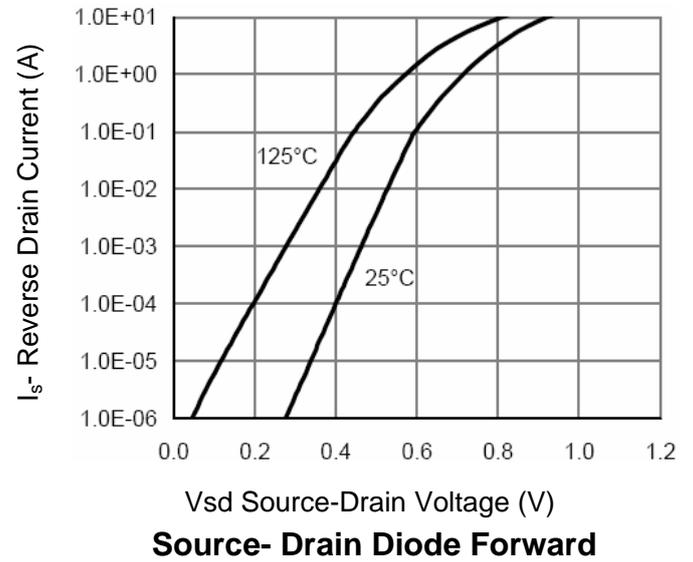
**Rds(on) vs Vgs**



**Capacitance vs Vds**



**Gate Charge**



**Source- Drain Diode Forward**

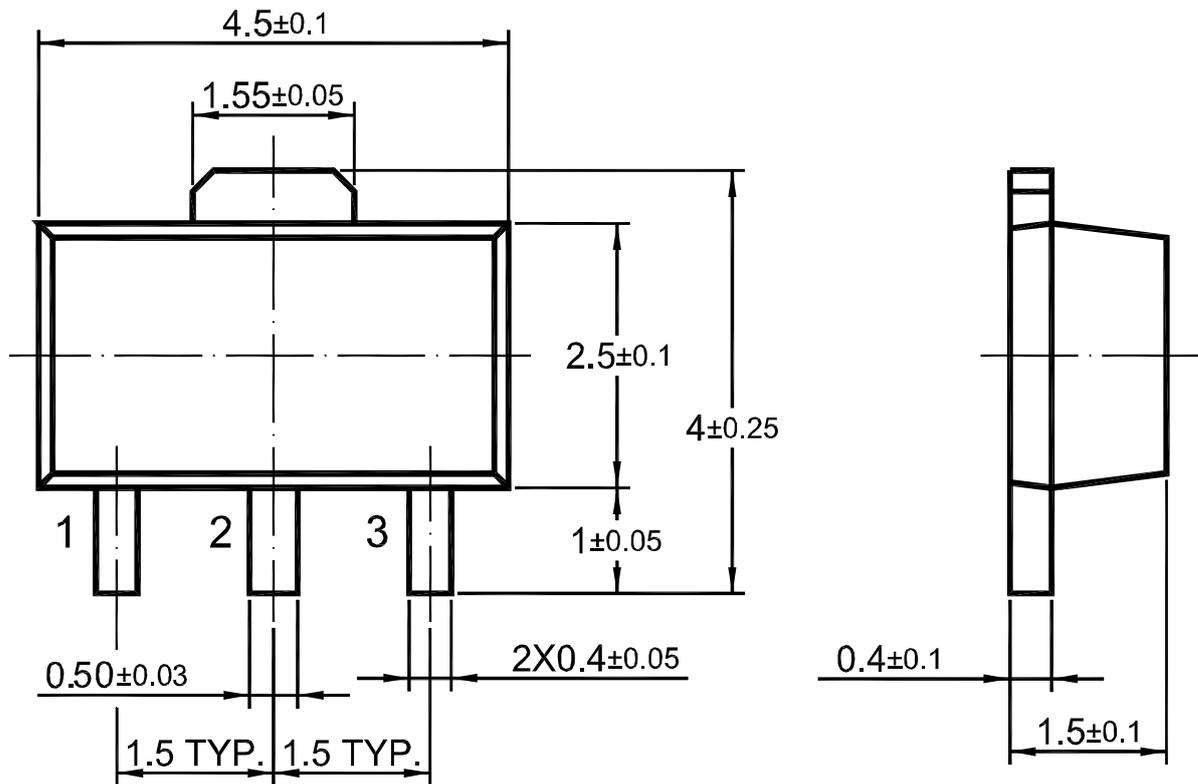


# PJM3400NSQ

## N- Enhancement Mode Field Effect Transistor

### Package Outline

SOT-89



### Ordering Information

Device	Package	Shipping
PJM3400NSQ	SOT-89	1000/Reel&Tape(7inch)