

General Description

The WSD80100DN56 is the highest performance trench N-Ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSD80100DN56 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

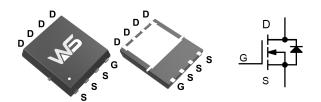
Product Summery

BV _{DSS}	R _{DSON}	I _D
80V	6.1mΩ	100A

Applications

- DC-DC converter switching for Networkong
- General purpose switching

DFN5X6-8 Pin Configuration



Absolute Maximum Ratings (T_A=25°C Unless Otherwise Noted)

Symbol	Parameter Rating		Units
V_{DS}	Drain-Source Voltage 80		V
V_{GS}	Gate-Source Voltage	±20	V
TJ	Maximum Junction Temperature 150		°C
I _D	Storage Temperature Range	-55 to 150	°C
	Continuous Drain Current, V _{GS} =10V,T _C =25°C	100	Α
I _D	Continuous Drain Current, V _{GS} =10V,T _C =100°C	80	Α
I _{DM}	Pulsed Drain Current ,T _C =25°C	Pulsed Drain Current ,T _C =25°C 380	
P _D	Maximum Power Dissipation, T _C =25°C 200		W
$R_{ heta JC}$	Thermal Resistance-Junction to Case 0.8		°C
E _{AS}	Avalanche Energy, Single pulse,L=0.5mH	800	mJ



Electrical Characteristics (T_J=25 C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	80			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =1mA		0.043		V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =40A		6.1	8.5	mΩ
$V_{GS(th)}$	Gate Threshold Voltage		2.0	3.0	4.0	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient			-6.94		mV/℃
I _{DSS}	Drain-Source Leakage Current	V_{DS} =48V , V_{GS} =0V , T_{J} =25 $^{\circ}$ C			2	- uA
		V_{DS} =48V , V_{GS} =0V , T_{J} =55 $^{\circ}$ C			10	
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm 20 V$, V_{DS} = $0 V$			±100	nA
gfs	Forward Transconductance	V _{DS} =5V , I _D =20A	80			S
Qg	Total Gate Charge (10V)	V _{DS} =30V , V _{GS} =10V , I _D =30A		125		nC
Q_gs	Gate-Source Charge			24		
Q_gd	Gate-Drain Charge			30		
$T_{d(on)}$	Turn-On Delay Time			20		
T_r	Rise Time	V _{DD} =30V , V _{GS} =10V ,		19		ns
T _{d(off)}	Turn-Off Delay Time	R_G =2.5Ω, I_D =2A ,RL=15Ω.		70		
T _f	Fall Time			30		
C _{iss}	Input Capacitance			4900		
C _{oss}	Output Capacitance	V _{DS} =25V , V _{GS} =0V , f=1MHz		410		pF
C _{rss}	Reverse Transfer Capacitance			315		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,6}	V _G =V _D =0V , Force Current			105	Α
I _{SM}	Pulsed Source Current ^{2,6}	VG-VD-UV , FOICE Current			400	Α
V_{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =40A , T _J =25℃			1.4	V

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- **4.** Guaranteed by design, not subject to production
- 5. EAS condition: Tj=25 $^{\circ}\text{C}$,VDD=40V,VG=10V,L=0.5mH,Rg=25 Ω



Typical Characteristics

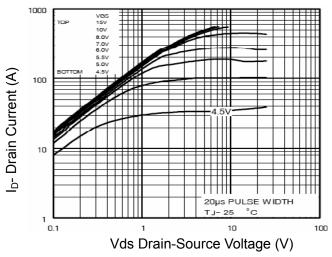


Figure 1 Output Characteristics

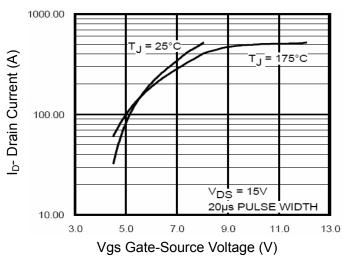


Figure 2 Transfer Characteristics

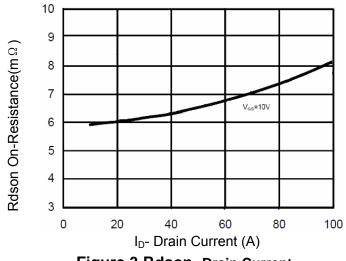


Figure 3 Rdson- Drain Current

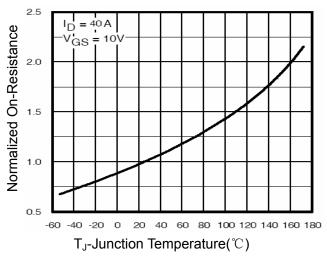


Figure 4 Rdson-JunctionTemperature

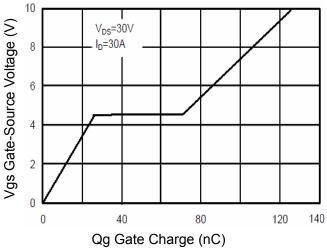


Figure 5 Gate Charge

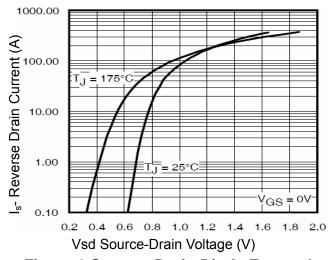


Figure 6 Source- Drain Diode Forward



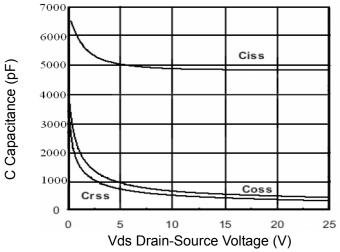


Figure 7 Capacitance vs Vds

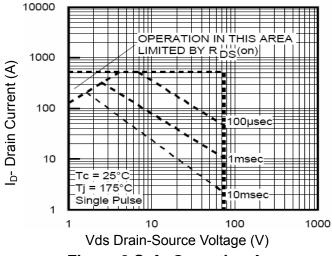


Figure 8 Safe Operation Area

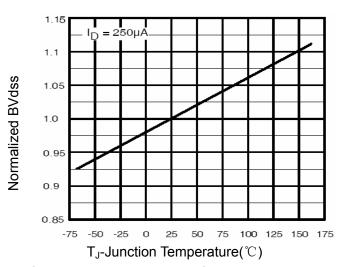


Figure 9 BV_{DSS} vs Junction Temperature

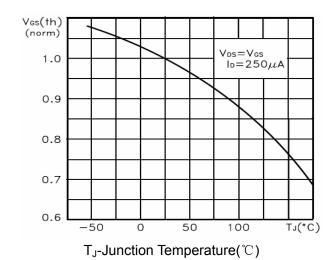


Figure 10 V_{GS(th)} vs Junction Temperature

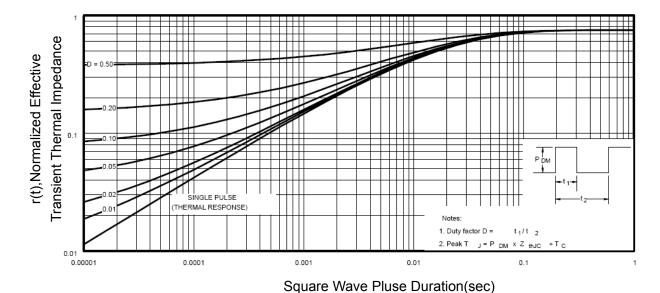


Figure 11 Normalized Maximum Transient Thermal Impedance



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